TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC540AP, TC74HC540AF TC74HC541AP, TC74HC541AF

Octal Bus Buffer

TC74HC540AP/AF Inverting, 3-State

Outputs

TC74HC541AP/AF Non-Inverting,

3-State Outputs

The TC74HC540A/TC74HC541A are high speed CMOS OCTAL BUS BUFFERs fabricated with silicon gate $\rm C^2MOS$ technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

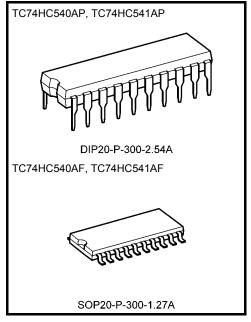
The TC74HC540A is an inverting type, and the TC74HC541A is a non-inverting type.

When either $\overline{G}1$ or $\overline{G}2$ are high, the terminal outputs are in the high-impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 10 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max)}$ at $T_a = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Output Drive Capability: 15 LSTTL loads
- Symmetrical output impedance: | I_{OH} | = I_{OL} = 6 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS540/541

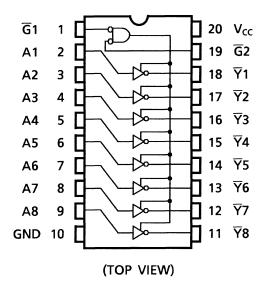


Weight

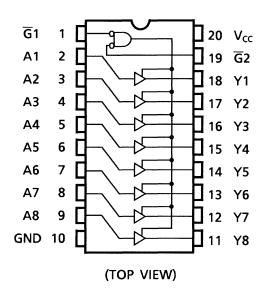
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

Pin Assignment

TC74HC540A

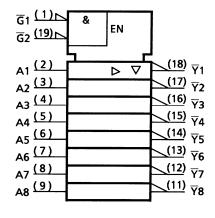


TC74HC541A

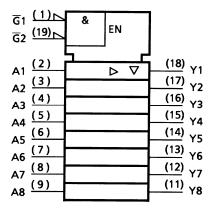


IEC Logic Symbol

TC74HC540A



TC74HC541A



Truth Table

	Inputs	Outputs			
G1	G2	An	Yn*	₹n *	
Н	Х	Х	Z	Z	
Х	Н	Х	Z	Z	
L	L	Н	Н	L	
L	L	L	L	Н	

X: Don't care

Z: High impedance

*: Yn..... HC541

Tn HC540



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	−0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±35	mA
DC V _{CC} /ground current	Icc	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	٧
Operating temperature	T _{opr}	−40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
		Vo		V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50	_	_	1.50	_	
High-level input voltage	V_{IH}			4.5	3.15	_	_	3.15	_	V
				6.0	4.20	_	_	4.20	_	
		_		2.0	_	_	0.50	_	0.50	
Low-level input voltage	V_{IL}			4.5	_	_	1.35	_	1.35	V
l				6.0	_	_	1.80	_	1.80	
	Voн			2.0	1.9	2.0	_	1.9	_	
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			I _{OH} = -6 mA	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	_	0.0	0.1	_	0.1	V
rollago			I _{OL} = 6 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 7.8 mA	6.0	_	0.18	0.26	_	0.33	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		6.0	_	_	±0.5	_	±5.0	μА
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		6.0	_	_	4.0	_	40.0	μА



AC Characteristics (input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit		
			CL (pF)	V _{CC} (V)	Min	Тур.	Max	Min	Max		
	t			2.0	_	25	60	_	75		
Output transition time	t _{TLH}	_	50	4.5	_	7	12	_	15	ns	
	t _{THL}			6.0		6	10	_	13		
				2.0	_	36	90	_	115		
			50	4.5	_	12	18	_	23		
Propagation delay	t_{pLH}	_		6.0		10	15	_	20	- ns	
time	t_{pHL}		150	2.0	_	51	130	_	165		
				4.5	_	17	26	_	33		
				6.0		14	22	_	28		
	t _{pZL} t _{pZH} R _L = 1	$R_L = 1 \text{ k}\Omega$	50	2.0	_	45	125	_	155	- ns	
				4.5	_	14	25	_	31		
Output enable time				6.0		12	21	_	26		
Output chable time			150	2.0	_	60	165	_	205		
				4.5	_	19	33	_	41		
				6.0		16	28	_	35		
	t_{pLZ} t_{pHZ} $R_L =$	$R_L = 1 \text{ k}\Omega$	50	2.0	_	40	125	_	155	ns	
Output disable time				4.5	_	16	25	_	31		
				6.0		14	21	_	26		
Input capacitance	C _{IN}	_			_	5	10		10	pF	
Output capacitance	C _{OUT}	-	_			10	_	_	_	pF	
Power dissipation	C _{PD}	TC74HC540A			32	_		_	pF		
capacitance	(Note)	TC74HC541A				35	_	_	_	μ	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

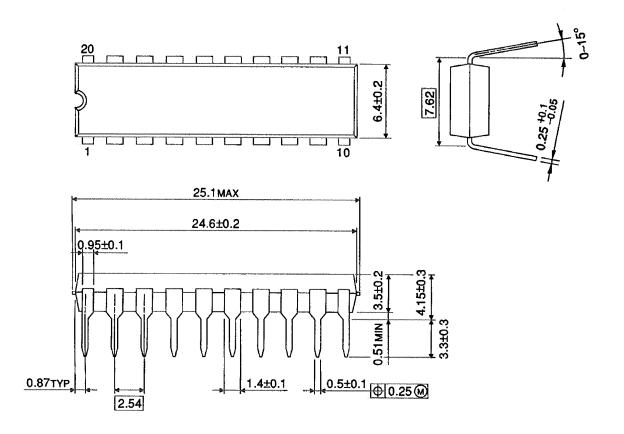
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Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$ (per bit)

Package Dimensions

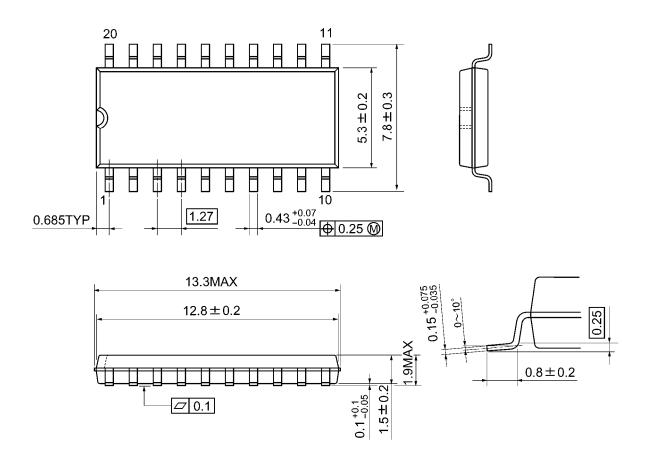
DIP20-P-300-2.54A Unit: mm



Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

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