imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



PER MIL-PRF-19500/452

- 6.4 VOLT NOMINAL ZENER VOLTAGE + 5%
- TEMPERATURE COMPENSATED ZENER REFERENCE DIODES
- METALLURGICALLY BONDED

MAXIMUM RATINGS

Operating Temperature: -65°C to +175°C Storage Temperature: -65°C to +175°C DC Power Dissipation: 500mW @ +50°C Power Derating: 4 mW / °C above +50°C

REVERSE LEAKAGE CURRENT

 $I_{R} = 2\mu A @ 25^{\circ}C \& V_{R} = 3Vdc$

ELECTRICAL CHARACTERISTICS @ 25°C, unless otherwise speci1/2ed.

JEDEC TYPE NUMBER	ZENER TEST CURRENT	EFFECTIVE TEMPERATURE COEFFICIENT	VOLTAGE TEMPERATURE STABILITY (³ V _{ZT} MAX) (Note 1)	TEMPERATURE RANGE	MAX. DYNAMIC ZENER IMPEDANCE (Note 2)
	mA	%/°C	mV	°C	OHMS
1N4565	.5	.01	48	0 to + 75°C	200
1N4565A	.5	.01	100	-55 to + 100°C	200
1N4566	.5	.005	24	0 to + 75°C	200
1N4566A	.5	.005	50	-55 to + 100°C	200
1N4567	.5	.002	10	0 to + 75°C	200
1N4567A	.5	.002	20	-55 to + 100°C	200
1N4568	.5	.001	5	0 to + 75°C	200
1N4568A	.5	.001	10	-55 to + 100°C	200
1N4569	.5	.0005	2.5	0 to + 75°C	200
1N4569A	.5	.0005	5	-55 to + 100°C	200
1N4570	1.0	.01	48	0 to + 75°C	100
1N4570A	1.0	.01	100	-55 to + 100°C	100
1N4571	1.0	.005	24	0 to + 75°C	100
1N4571A	1.0	.005	50	-55 to + 100°C	100
1N4572	1.0	.002	10	0 to + 75°C	100
1N4572A	1.0	.002	20	-55 to + 100°C	100
1N4573	1.0	.001	5	0 to + 75°C	100
1N4573A	1.0	.001	10	-55 to + 100°C	100
1N4574	1.0	.0005	2.5	0 to + 75°C	100
1N4574A	1.0	.0005	5	-55 to + 100°C	100
1N4575	2.0	.01	48	0 to + 75°C	50
1N4575A	2.0	.01	100	-55 to + 100°C	50
1N4576	2.0	.005	24	0 to + 75°C	50
1N4576A	2.0	.005	50	-55 to + 100°C	50
1N4577	2.0	.002	10	0 to + 75°C	50
1N4577A	2.0	.002	20	-55 to + 100°C	50
1N4578	2.0	.001	5	0 to + 75°C	50
1N4578A	2.0	.001	10	-55 to + 100°C	50
1N4579	2.0	.0005	2.5	0 to + 75°C	50
1N4579A	2.0	.0005	5	-55 to + 100°C	50
1N4580	4.0	.01	48	0 to + 75°C	25
1N4580A	4.0	.01	100	-55 to + 100°C	25
1N4581	4.0	.005	24	0 to + 75°C	25
1N4581A	4.0	.005	50	-55 to + 100°C	25
1N4582	4.0	.002	10	0 to + 75°C	25
1N4582A	4.0	.002	20	-55 to + 100°C	25
1N4583	4.0	.001	5	0 to + 75°C	25
1N4583A	4.0	.001	10	-55 to + 100°C	25
1N4584	4.0	.0005	2.5	0 to + 75°C	25
1N4584A	4.0	.0005	5	-55 to + 100°C	25

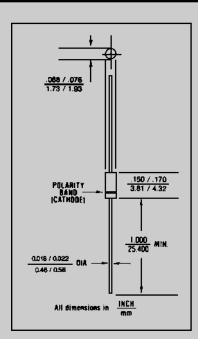


FIGURE 1

DESIGN DATA

CASE: Hermetically sealed glass case. DO - 35 outline.

LEAD MATERIAL: Copper clad steel.

LEAD FINISH: Tin / Lead

POLARITY: Diode to be operated with the banded (cathode) end positive.

MOUNTING POSITION: ANY.

NOTE 1 The maximum allowable change observed over the entire temperature range i.e., the diode voltage will not exceed the speci1/2ed mV at any discrete temperature between the established limits, per JEDEC standard No.5.

NOTE 2 Zener impedance is derived by superimposing on I_{7T} A 60Hz rms a.c. current equal to 10% of I_{7T}.



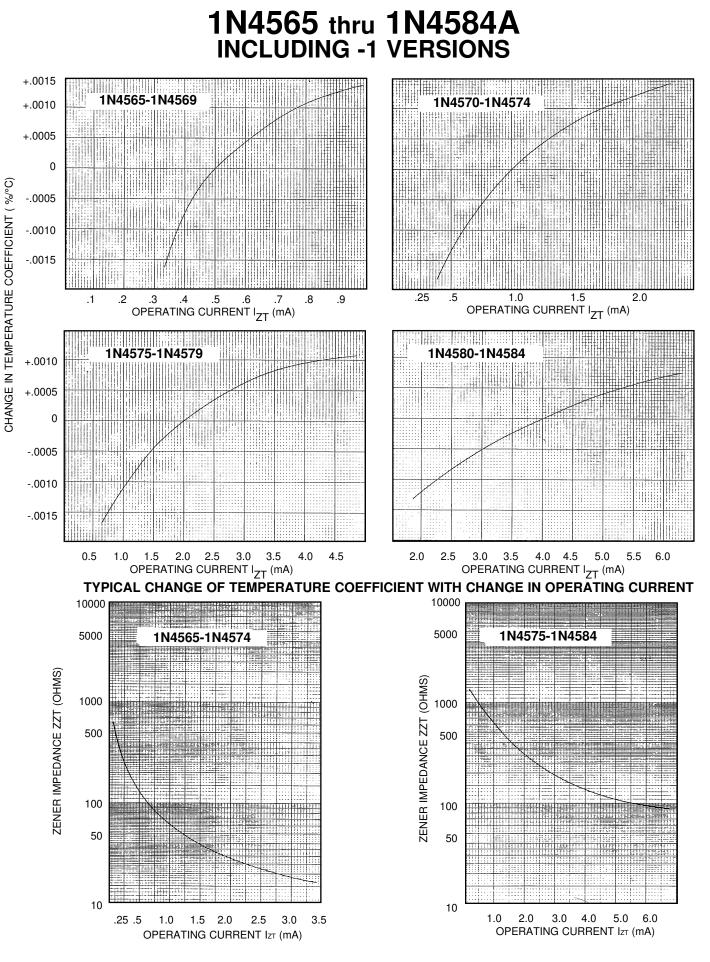
6 LAKE STREET, LAWRENCE, MASSACHUSETTS 01841 PHONE (978) 620-2600 WEBSITE: http://www.microsemi.com

FAX (978) 689-0803

1N4565 thru 1N4584A

and

1N4565A-1 thru 1N4584A-1



ZENER IMPEDANCE VS. OPERATING CURRENT