NCV8664 Evaluation Board User's Manual



ON Semiconductor®

http://onsemi.com

EVAL BOARD USER'S MANUAL

Description

The NCV8664 is a precision 3.3 V and 5.0 V fixed output, low dropout integrated voltage regulator with an output current capability of 150 mA. Careful management of light load current consumption, combined with a low leakage process, achieve a typical quiescent ground current of 22 μA . The output voltage is accurate within $\pm 2.0\%$, and maximum dropout voltage is 600 mV at full rated load current. The following ceramic capacitors are the recommended values to be used with these devices; $C_{in}=0.1~\mu F,~C_{out}=10~\mu F.$

Features

- ±2.0% Output Accuracy, Over Full Temperature Range
- 30 μA Maximum Quiescent Current at I_{out} = 100 μA
- 600 mV Maximum Dropout Voltage at 150 mA Load Current

- Wide Input Voltage Operating Range of 5.5 V to 45 V
- Internal Fault Protection
 - −42 V Reverse Voltage
 - Short Circuit/Overcurrent
 - Thermal Overload

Board Notes

Max voltage on V_{in} cap not to exceed 35 V.

Board Layouts

These boards are shown in sets of 2 due to the minimum board size requirement of most board fabrication houses. When sent out for fabrication, it must be indicated that the center line of the board set be V-scored to allow board separation.



Figure 1. Evaluation Board Photo

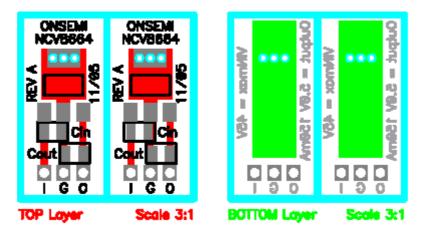


Figure 2. SOT-223 Evaluation Board

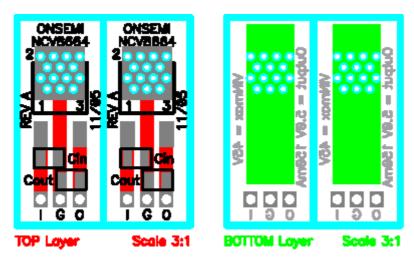


Figure 3. DPAK Evaluation Board

SCHEMATIC FOR THE NCV8664 EVALUATION BOARD

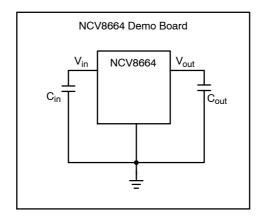


Figure 4. NCV8664 Evaluation Board Circuit

Table 1. BILL OF MATERIALS FOR THE NCV8664 EVALUATION BOARD

Table 1. Bill of Mixtelliaes for the Novosof Evaluation Board						
Value	Tolerance	Footprint	Manufacturer	Part Number	Substitution Allowed	Lead Free
SOT223 B	ОМ			•		
-	_	-	Advanced Circuits	NCV8664DPAK3DemoBoard	No	Yes
-	-	SOT223	On Semiconductor	NCV8664ST50R3G	No	Yes
-	-	-	Molex/Waldom Electronics Corporation	22-28-8030	Yes	Yes
0.1 μF	10%	1206	Murata Electronics North America	GRM319R71H104KA01D	Yes	Yes
10 μF	10%	1206	Murata Electronics North America	GRM31CR71C106KAC7L	Yes	Yes
DPAK BO	М					
=	-	-	Advanced Circuits	NCV8664DPAK3DemoBoard	No	Yes
=	-	DPAK	On Semiconductor	NCV8664DT50RKG	No	Yes
-	-	-	Molex/Waldom Electronics Corporation	22-28-8030	Yes	Yes
0.1 μF	10%	1206	Murata Electronics North America	GRM319R71H104KA01D	Yes	Yes
10 μF	10%	1206	Murata Electronics North America	GRM31CR71C106KAC7L	Yes	Yes

Test Procedure

Required Equipment:

- Resistive Load
- 2 Multimeters

- One NCV8664 Evaluation Board
- DC Power Supply

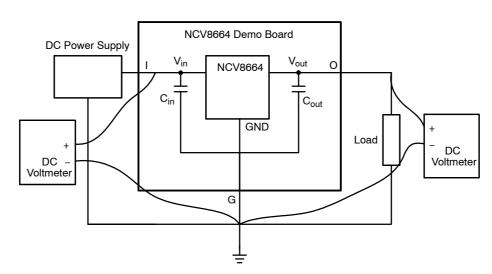


Figure 5. Dropout Voltage Test Setup

Dropout Voltage Verification Steps

- 1. Connect circuit as shown in Figure 5.
- 2. Set $V_{in} = 13.5 \text{ V}$, Record V_{out} .

- 3. Reduce V_{in} until V_{out} has dropped by 100 mV.
- 4. Subtract V_{out} from V_{in} . Resulting Voltage is Dropout Voltage.

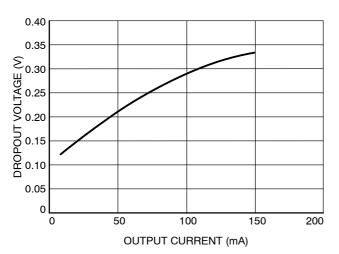


Figure 6. Dropout Voltage vs. Output Current

Quiescent Current Verification Steps

- 1. Connect circuit as shown in Figure 7.
- 2. Set $V_{in} = 13.5 \text{ V}$.
- 3. Subtract Output Current from Input Current.

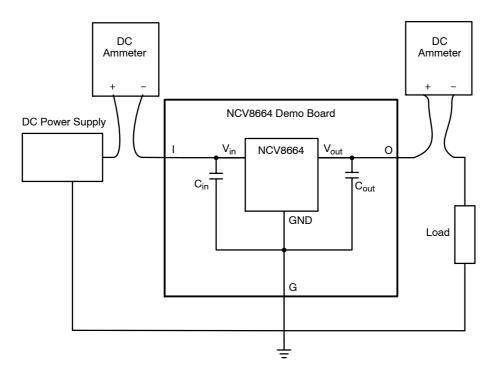


Figure 7. Quiescent Current Verification Setup

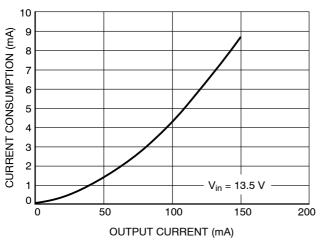


Figure 8. Current Consumption vs. Output Current

Output Voltage Verification Steps

- 1. Connect circuit as shown in Figure 9.
- 2. Set output load to 100 Ω , Set V_{in} = 0 V, Record V_{out} .
- 3. Increase V_{in} , measure V_{out} .

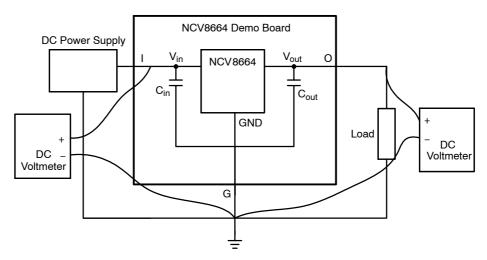


Figure 9. Quiescent Current Verification Setup

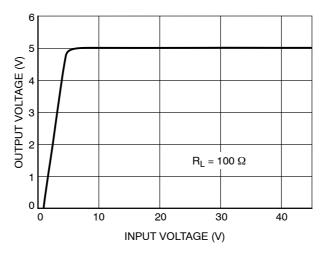


Figure 10. Input Voltage vs. Output Voltage

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

N. American Technical Support: 800-282-9855 Toll Free

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910

Japan Customer Focus Center
Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative