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Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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MX554EBC20M0000

Ultra-Low Jitter 20MHz LVCMOS XO

ClockWorks® FUSION

General Description

The MX554EBC20M0000 is an ultra-low phase jitter XO with LVCMOS output optimized for high line rate applications.

Features

- 20MHz LVCMOS
- Typical phase noise:
 - 83fs (Integration range: 1.875MHz-5MHz)
- ±50ppm total frequency stability
- -40°C to +85°C temperature range
- Industry standard 6-Pin 5mm x 3.2mm LGA package

Absolute Maximum Ratings

Supply Voltage (VIN)	+4.6V
Lead Temperature (soldering, 10s)	
Storage Temperature (T _s)	125°C
ESD Rating (HBM)	

Electrical Characteristics

VDD = 2.375 - 3.63V, TA = $-40^{\circ}C$ to $+85^{\circ}C$, output terminated with 50 Ohms to VDD/2.¹

Units Symbol Parameter Condition Min. Typ. Max. IDD Supply Current 95 mА F0 Center Frequency 20 MHz Frequency Stability ± 50 Note 2 ppm 136 Integration Range (12kHz to 5MHz) Øj Phase Noise fsRMS Integration Range (1.875MHz to 5MHz) 83 Start-Up Time Tstart 20 ms TR/TF Rise/Fall time 100 500 ps 45 Duty Cycle 55 % 2 VIH V Input High Voltage 3.3V Operation VDD + 0.3VIL Input Low Voltage 3.3V Operation -0.3 0.8 V VOH Output High Voltage LVCMOS output levels VDD - 0.8 V VOL Output Low Voltage LVCMOS output levels 0.6 V

Notes:

1. Guaranteed after thermal equilibrium.

2. Inclusive of initial accuracy, temperature drift, aging, shock, vibration.

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Microchip Technology Inc.

August 18, 2016 MX554EB1-4235 http://www.microchip.com

Revision 1.0 tcghelp@microchip.com

Operating Ratings

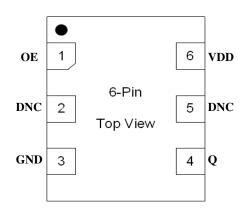
Supply Voltage (VIN).....+2.375V to +3.63V Ambient Temperature (TA).....-40°C to +85°C

Ordering Information

Ordering Part Number	Marking Line 1	Marking Line 3	Shipping	Package
MX554EBC20M0000	MX554E	BC0200	Tube	6-Pin 5mm x 3.2mm LGA
MX554EBC20M0000 TR	MX554E	BC0200	Tape and Reel	6-Pin 5mm x 3.2mm LGA

Devices are Green and RoHS compliant. Sample material may have only a partial top mark.

Pin Configuration



Pin Description

Pin Number	Pin Name	Pin Type	Pin Level	Pin Function
1	OE	I, SE	LVCMOS	Output Enable, disables output to tri-state, 0 = Disabled, 1 = Enabled, 50k Ohms Pull-Up
2	DNC			Make no connection, leave floating.
3	GND	PWR		Power Supply Ground
4, 5	Q, DNC	O, SE	LVCMOS	Clock Output Frequency = 20MHz
6	VDD	PWR		Power Supply

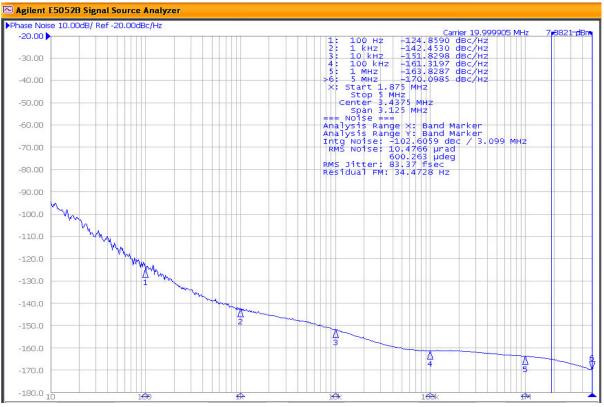


Figure 1. LVCMOS Output 20MHz 1.875MHz-5MHz 83fs

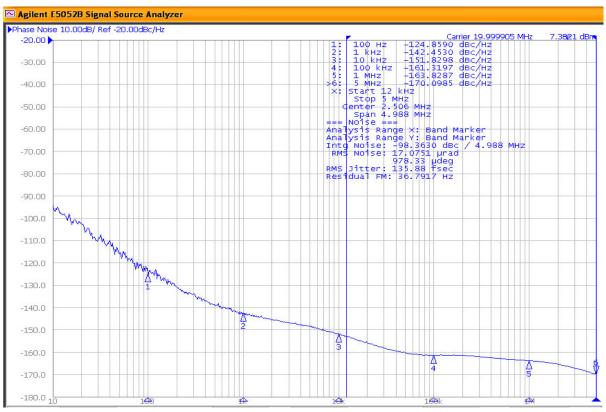
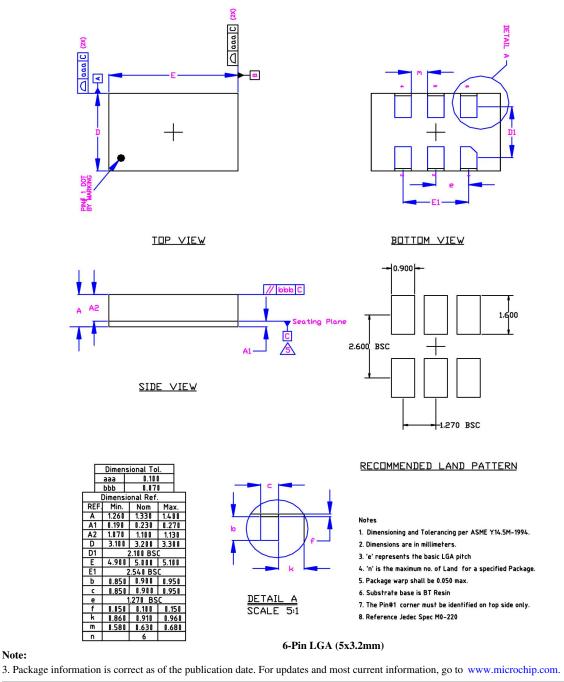


Figure 2. LVCMOS Output 20MHz 12kHz-5MHz 136fs

Package Information and Recommended Land Pattern for 6-Pin LGA³



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Note: