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# **ECH8419**

# ON Semiconductor®

# http://onsemi.com

# **N-Channel Power MOSFET** 35V, 9A, 17mΩ, Single ECH8

### **Features**

- ON-resistance RDS(on)1=13m $\Omega$  (typ.)
- · 4V drive
- · Halogen free compliance
- · Protection diode in

## **Specifications**

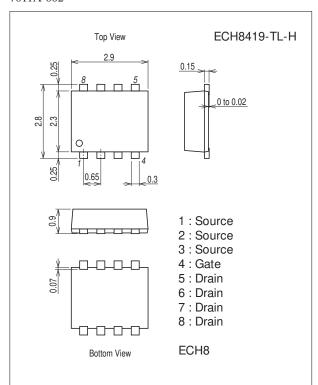
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		35	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		9	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	Α
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm <sup>2</sup> ×0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Package Dimensions**

unit: mm (typ) 7011A-002

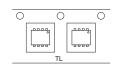


### **Product & Package Information**

• Package : ECH8 • JEITA, JEDEC

• Minimum Packing Quantity : 3,000 pcs./reel

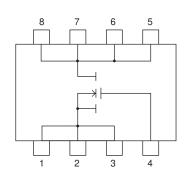
## Packing Type: TL





Marking

#### **Electrical Connection**

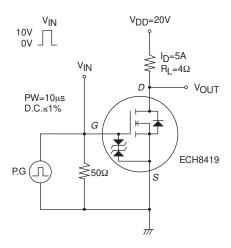


# ECH8419

## **Electrical Characteristics** at Ta=25°C

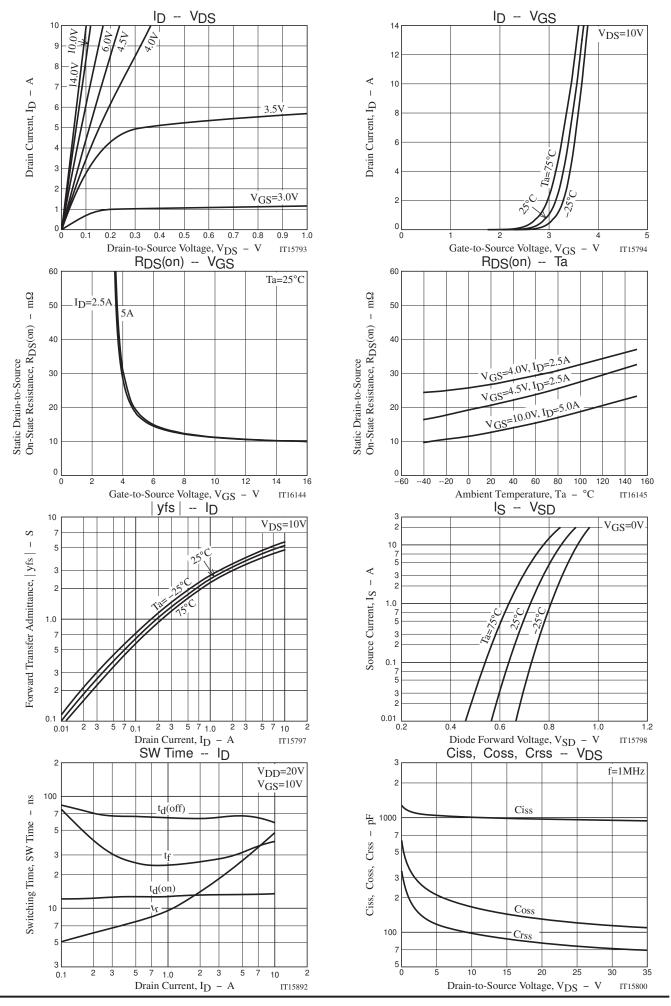
Parameter	Symbol	Conditions	Ratings			Unit	
Farameter	Syllibol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	35			V	
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =35V, V <sub>GS</sub> =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ	
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.2		2.6	V	
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =5A		4.3		S	
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =5A, V <sub>G</sub> S=10V		13	17	mΩ	
	R <sub>DS</sub> (on)2	I <sub>D</sub> =2.5A, V <sub>G</sub> S=4.5V		21	30	mΩ	
	R <sub>DS</sub> (on)3	I <sub>D</sub> =2.5A, V <sub>G</sub> S=4V		27	38	mΩ	
Input Capacitance	Ciss			960		pF	
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		130		pF	
Reverse Transfer Capacitance	Crss			80		pF	
Turn-ON Delay Time	t <sub>d</sub> (on)			13		ns	
Rise Time	t <sub>r</sub>	Considered Took Consult		26		ns	
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		66		ns	
Fall Time	tf			31		ns	
Total Gate Charge	Qg			19		nC	
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =20V, V <sub>GS</sub> =10V, I <sub>D</sub> =9A		3.9		nC	
Gate-to-Drain "Miller" Charge	Qgd	1		3.8		nC	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =9A, V <sub>GS</sub> =0V		0.85	1.2	V	

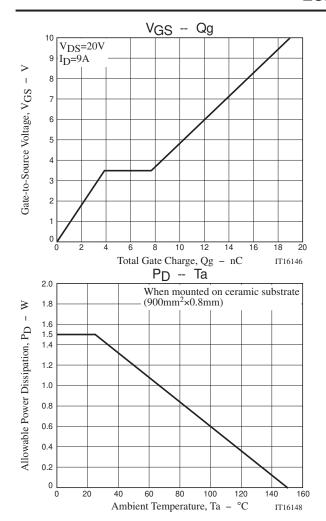
# **Switching Time Test Circuit**

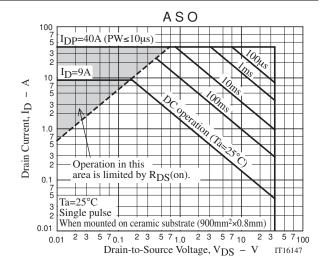


# **Ordering Information**

Device	Package	Shipping	memo	
ECH8419-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free	





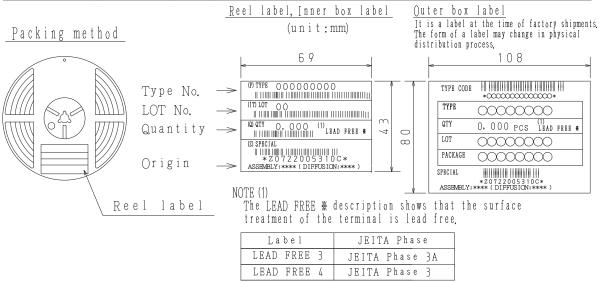


#### **Embossed Taping Specification**

#### ECH8419-TL-H

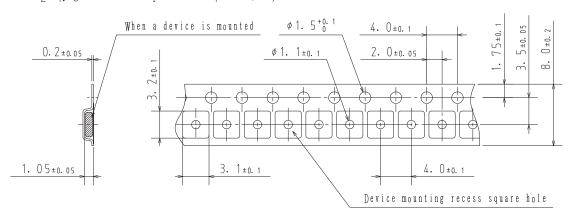
## 1. Packing Format

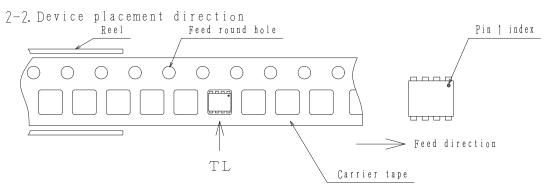
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)
ECH8	СРН6	3, 000	15, 000	90,000	5 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210



## 2. Taping configuration

2-1. Carrier tape size (unit:mm)





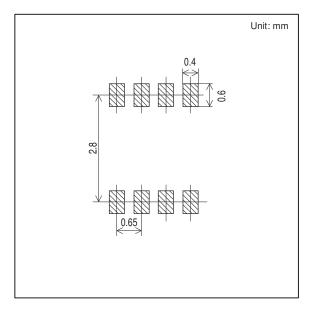
Those with pin 1 index on the feed hole side · · · · · TL

# **Outline Drawing**

ECH8419-TL-H

# 

# **Land Pattern Example**



Note on usage: Since the ECH8419 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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