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COMPLEMENTARY 30V ENHANCEMENT MODE MOSFET H-BRIDGE

Product Summary

Device	BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
N-Channel	30V	0.12Ω @ V _{GS} = 10V	3.1A
P-Channel	-30V	0.21Ω @ V _{GS} = -10V	-2.3A

Description

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

Applications

• Single Phase DC Fan Motor Drive

Features

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Single SM-8 Surface Mount Package
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SM-8 (8 LEAD SOT223)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.117 grams (Approximate)

ZXMHC3A01 = Product Type Marking Code

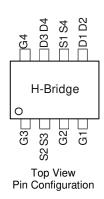
Y or \overline{Y} = Last Digit of Year (ex: 5= 2015)

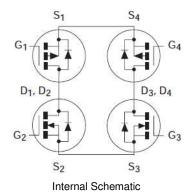
WW or $\overline{W}W$ = Week Code (01~53)

YWW = Date Code Marking



Top View





Ordering Information (Note 4)

Part Number	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMHC3A01T8TA	7"	12mm	1,000 units

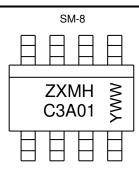
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



ZXMHC3A01T8 Document number: DS33505 Rev. 6 - 2



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteris	Symbol	N-channel	P-channel	Units		
Drain-Source Voltage	V _{DSS}	30	-30	V		
Gate-Source Voltage	V _{GSS}	±20	±20	V		
Continuous Drain Current, V _{GS} = 10V (Note 8)	Steady State	$T_{A} = +25^{\circ}C \text{ (Note 6 \& 8)}$ $T_{A} = +70^{\circ}C \text{ (Note 6 \& 8)}$ $T_{A} = +25^{\circ}C \text{ (Note 5 \& 8)}$	ID	3.1 2.5 2.7	-2.3 -1.8 -2.0	А
Continuous Source Current (body diode) (Note 6)	ls	2.3	-2.2	А		
Pulsed Drain Current (Note 7)				14.3	-10.8	А
Pulsed Source Current (Note 7)				14.5	-10.8	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 8) Linear Derating Factor	$T_{A} = +25^{\circ}C$ (Note 5)	PD	1.3 10.4	W mW/°C
Total Power Dissipation (Note 8) Linear Derating Factor	T _A = +25°C (Note 6)	PD	1.7 13.6	W mW/°C
Thermal Resistance, Junction to Ambient (Note 8)	Steady State (Note 5)	Р	96	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State (Note 6)	R _{0JA}	73	°C/W
Operating and Storage Temperature Range			-55 to +150	°C

Notes: 5. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions...

6. For a device surface mounted on FR4 PCB measured at t ≤10 seconds.
7. Repetitive rating 50mm x 50mm x 1.6mm FR4 PCB, D = 0.02, pulse width 300µs - pulse width limited by maximum junction temperature. Refer to transient thermal Impedance graph.

8. For device with one active die.



Electrical Characteristics N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	30		—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	—		1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	—		100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	$V_{DS}=V_{GS},\ I_{D}=250\mu A$
Static Drain-Source On-Resistance (Note 9)	_		_	0.12	Ω	$V_{GS} = 10V, I_D = 2.5A$
Static Drain-Source On-Resistance (Note 9)	R _{DS} (ON)	—	_	0.18	12	$V_{GS} = 4.5V, \ I_D = 2.0A$
Forward Transfer Admittance (Notes 9 & 11)	g _{fs}	_	3.5	_	S	$V_{DS} = 4.5V, I_D = 2.5A$
Diode Forward Voltage (Note 9)	V _{SD}	—		0.95	V	$V_{GS} = 0V, I_{S} = 1.7A$
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	Ciss	—	190	—	pF	$\label{eq:VDS} \begin{array}{l} V_{DS} = 25V, \ V_{GS} = 0V, \\ f = 1MHz \end{array}$
Output Capacitance	Coss		38	-		
Reverse Transfer Capacitance	C _{rss}	—	20	—		
Total Gate Charge (Note 10)	Qg	—	3.9	-		V_{DS} = 15V, I _D = 2.5A, V _{GS} = 10V
Gate-Source Charge (Note 10)	Q _{gs}		0.6	-	nC	
Gate-Drain Charge (Note 10)	Q _{gd}	—	0.9	—		
Turn-On Delay Time (Note 10)	t _{D(on)}		1.7	-		
Turn-On Rise Time (Note 10)	tr	_	2.3	—	ns	$\label{eq:VDD} \begin{split} V_{DD} &= 15V, \ V_{GS} = 10V, \\ I_D &= 2.5A, \ \ R_G \cong 6.0\Omega, \end{split}$
Turn-Off Delay Time (Note 10)	t _{D(off)}		6.6	-		
Turn-Off Fall Time	t _f	_	2.9	_		
Reverse Recovery Time	t _{rr}		17.7	_	ns	
Reverse Recovery Charge	Qrr	—	13	—	nC	I _S = 1.8A, di/dt = 100A/μs

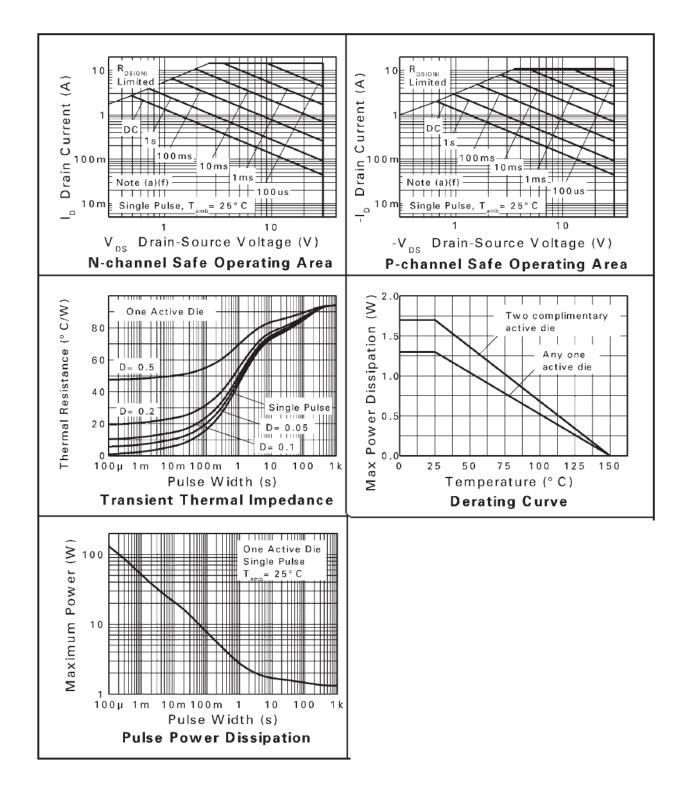
Electrical Characteristics P-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS			-			
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—	—	V	$V_{GS} = 0V, I_D = -250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	-	—	-1.0	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(th)}	-1.0	—	-3.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance (Note 9)	Deserve	_	—	0.21	Ω	$V_{GS} = -10V, I_D = -1.4A$
Static Drain-Source On-Resistance (Note 9)	R _{DS} (ON)		—	0.33	12	$V_{GS} = -4.5V, I_D = -1.1A$
Forward Transfer Admittance (Notes 9 & 11)	g fs	_	2.5	—	S	$V_{DS} = -15V, I_D = -1.4A$
Diode Forward Voltage (Note 9)	V _{SD}	—	-0.85	-0.95	V	$V_{GS} = 0V, I_{S} = -1.1A$
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	Ciss		204	_	pF	
Output Capacitance	Coss		39.8	_	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1MHz
Reverse Transfer Capacitance	C _{rss}		25.8	_	pF	
Gate Charge (V _{GS} = -5.0V) (Note 10)	Qg		2.6	_	nC	
Total Gate Charge (V _{GS} = -10V) (Note 10)	Qg		5.2	_	nC	V _{DS} = -15V, I _D = -1.4A,
Gate-Source Charge (Note 10)	Q _{gs}	—	0.7	_	nC	$v_{DS} = -15v, I_D = -1.4A,$
Gate-Drain Charge (Note 10)	Q _{gd}	_	0.9	—	nC	
Turn-On Delay Time (Note 10)	t _{D(on)}	—	1.2	—	ns	
Turn-On Rise Time (Note 10)	tr	_	2.3	—	ns	$V_{DD} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time (Note 10)	t _{D(off)}		12.1	—	ns	$R_G \cong 6.0\Omega, I_D = -1.0A$
Turn-Off Fall Time	t _f		7.5	_	ns	
Reverse Recovery Time	t _{rr}		19	—	ns	
Reverse Recovery Charge	Q _{rr}		15	_	nC	I _S = -0.95A, di/dt = 100A/µs

Notes:

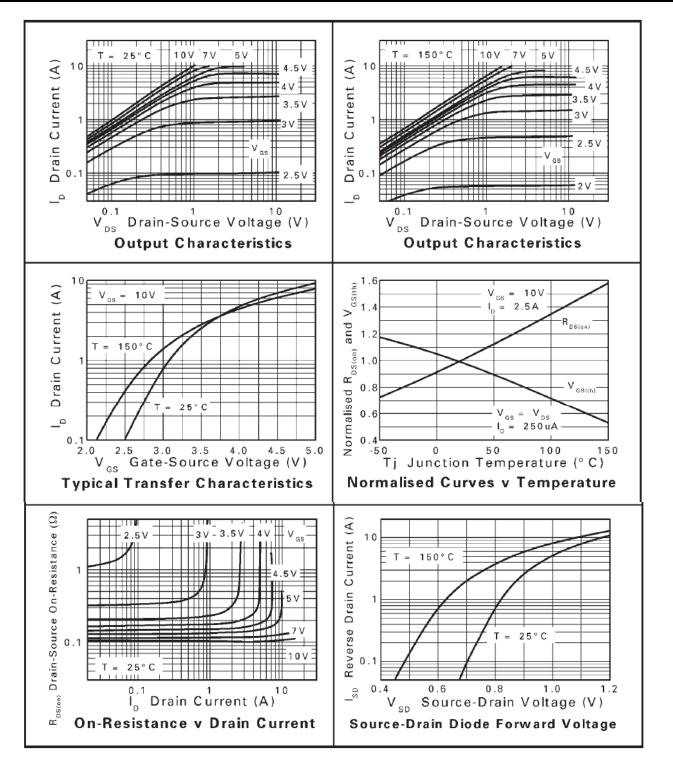
9. Measured under pulsed conditions. Width≤300µs. Duty cycle ≤ 2%.
 10. Switching characteristics are independent of operating junction temperature.
 11. For design aid only, not subject to production testing.





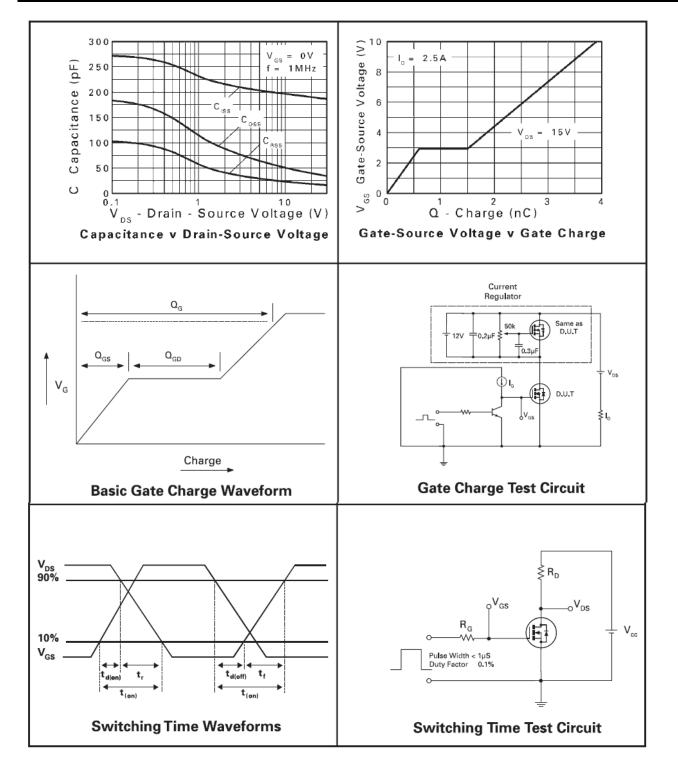


Typical Characteristics N-CHANNEL



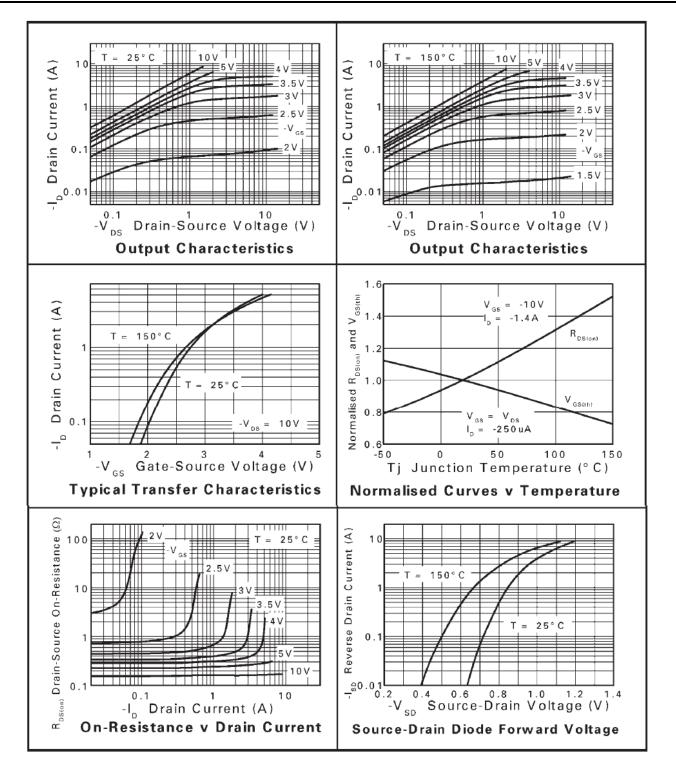


Typical Characteristics N-CHANNEL



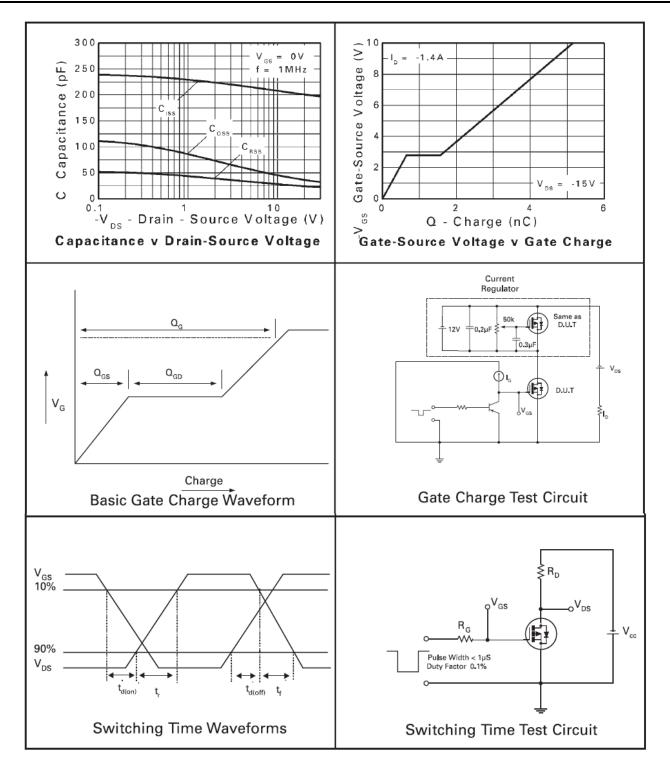


Typical Characteristics P-CHANNEL





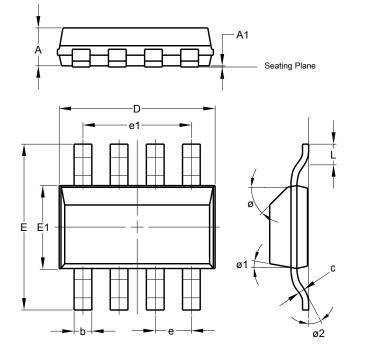
Typical Characteristics P-CHANNEL





Package Outline Dimensions

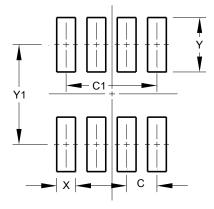
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SM-8						
Dim	Min Max Typ					
Α		1.70	1.60			
A1	0.02	0.10	0.04			
b	0.70	0.90	0.80			
С	0.24	0.24 0.32 0.28				
D	6.30 6.70 6.60					
е	1.53 REF					
e1	4.59 REF					
Е	6.70 7.30 7.00					
E1	3.30	3.70	3.50			
L	0.75	1.00	0.90			
Ø	45°					
Ø1		15°				
Ø2			10°			
All I	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.52
C1	4.60
Х	0.95
Y	2.80
Y1	6.80



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