

LD2979

Very low drop voltage regulators with inhibit

The LD2979 is a very low drop regulator available

The very low drop-voltage and the very low quiescent current make them particularly suitable for low noise, low power applications and in

Shutdown logic control function is available on five pin version (TTL compatible). This means that when the device is used as local regulator, it is possible to put a part of the board in standby, decreasing the total power consumption.

Description

battery powered systems.

in SOT23-5L.

Datasheet - production data



Features

- Very low dropout voltage (0.2 V typ. at 50 mA load)
- Very low quiescent current (typ. 500 μA at 50 mA load)
- Output current up to 50 mA
- Logic-controlled electronic shutdown
- Output voltages of 3.0 and 3.3 V
- Internal current and thermal limit
- Supply voltage rejection: 63 dB (typ)
- Only 1 µF for stability
- Temperature range: -25 °C to 125 °C
- Package available: SOT23-5L

Table 1. Device summary

Order codes	Output voltages
LD2979M30TR	3.0 V
LD2979M33TR	3.3 V

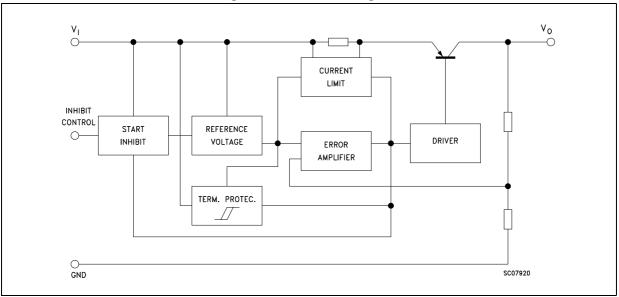
This is information on a product in full production.

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1 Diagram







2 Pin configuration

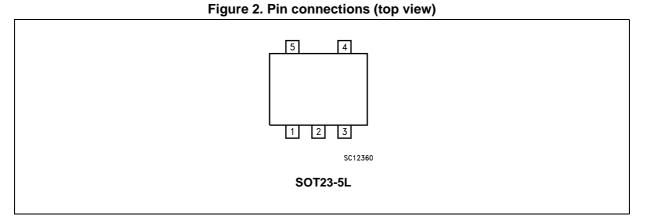


Table 2. Pin description

Symbol	Name and function	Pin number
VI	Input voltage	1
GND	Ground	2
INHIBIT	Control switch ON/OFF ⁽¹⁾	3
NC	Not to be connected	4
Vo	Output voltage	5

1. Inhibit pin is not internally pulled-up then it must not be left floating. Connect to a positive voltage higher than 2 V to able the device.



3 Maximum ratings

Symbol	Parameter	Value	Unit
VI	DC input voltage	16	V
V _{INH}	DC inhibit input voltage	V _{IN}	V
۱ ₀	Output current	Internally limited	
P _D	Power dissipation	Internally limited	
T _{STG}	Storage temperature range	-40 to 150	°C
T _{OP}	Operating junction temperature range	-25 to 125	°C

Table 3. Absolute maximum ratings

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.



4 Electrical characteristics

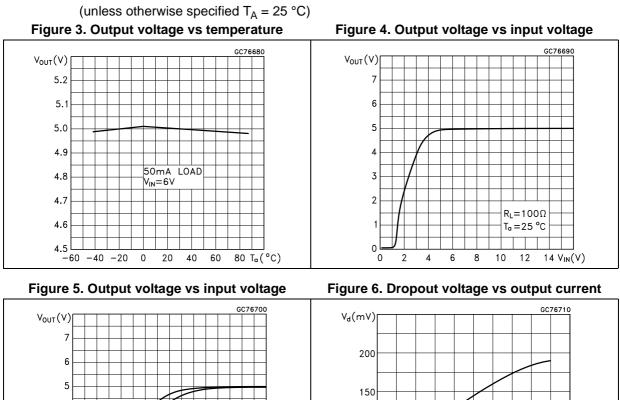
(Refer to the test circuits, Ta = 25 °C, $V_{IN} = V_{O(NOM)} + 1$ V, $I_O = 1$ mA, $V_{INH} = 2$ V, $C_O = 1$ µF, unless otherwise specified).

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
V _O	Output voltage	$V_{IN} = 4 V$	2.940	3	3.060	V	
		$I_{O} = 1$ to 50mA, $T_{a} = -25$ to 125°C	2.880		3.120		
		V _{IN} = 4.3 V	3.234	3.3	3.366	V	
۷O	V _O Output voltage	$I_{O} = 1$ to 50mA, $T_{a} = -25$ to 125°C	3.168		3.432		
Ι _Ο	Output current limit		100			mA	
A) /	Line regulation	$V_{IN} = V_{O(NOM)}$ +1V to 16V, $I_O = 1$ mA			0.028	%/V _{IN}	
ΔV_{O}	Line regulation	T _a = -25 to 125°C			0.064		
		I _O = 0		80	110		
	Quiescent current (On	$I_{O} = 0, T_{a} = -25 \text{ to } 125^{\circ}\text{C}$			170		
	Mode)	I _O = 50mA		500	700	μA	
I _d		$I_{O} = 50$ mA, T_{a} = -25 to 125°C			1300		
	Quiescent current (Off	V _{INH} < 0.18 V		0			
	Mode)	V _{INH} < 0.18 V, T _a = -25 to 125°C			1	μA	
SVR	Supply voltage rejection	$I_{O} = 50$ mA, $C_{OUT} = 10\mu$ F, f = 120Hz		63		dB	
		I _O = 0		6	12		
		$I_{\rm O} = 0, T_{\rm a} = -25 \text{ to } 125^{\circ}\text{C}$			18		
		I _O = 1mA		30	60	- mV	
V	Dranaut voltage	$I_{O} = 1mA, T_{a} = -25 \text{ to } 125^{\circ}C$			90		
V _d	Dropout voltage	I _O = 10mA		100	200		
		$I_{O} = 10$ mA, $T_{a} = -25$ to 125° C			300		
		I _O = 50mA		200	400		
		$I_{O} = 50$ mA, T_{a} = -25 to 125°C			600		
V _{IL}	Inhibit input logic low	Device Off, T _a = -25 to 125°C			0.18	V	
V _{IH}	Inhibit input logic high	Device On, T _a = -25 to 125°C	2			V	
1	Inhibit input current	V _{INH} = 0 V		0	-1		
ų		$V_{INH} = 5V, T_a = -25 \text{ to } 125^{\circ}\text{C}$		5	15	- μΑ	
eN	Output noise voltage (RMS)	BW= 300Hz to 50kHz, $C_0 = 10\mu F$		160		μV	





5 Typical characteristics



100

50

0

10

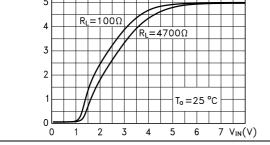


Figure 7. Dropout voltage vs temperature

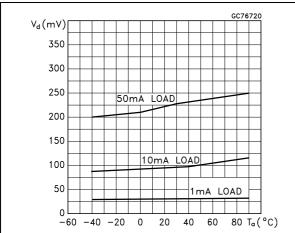


Figure 8. Quiescent current vs temperature

20

30

40

l₀ut(mA)

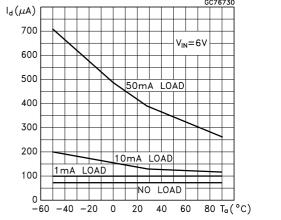




Figure 9. Short circuit current vs dropout voltage

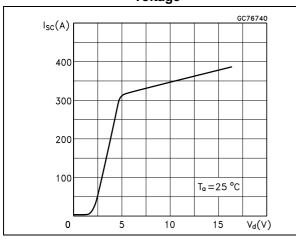


Figure 11. Supply voltage rejection vs frequency

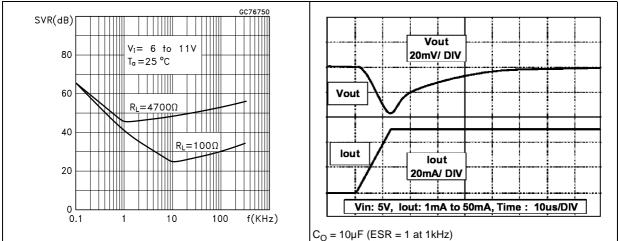


Figure 10. Inhibit voltage vs temperature

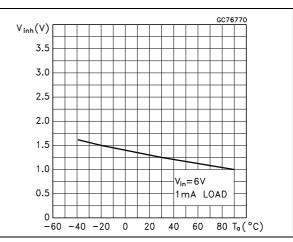


Figure 12. Load transient response

Figure 13. Inhibit current vs temperature

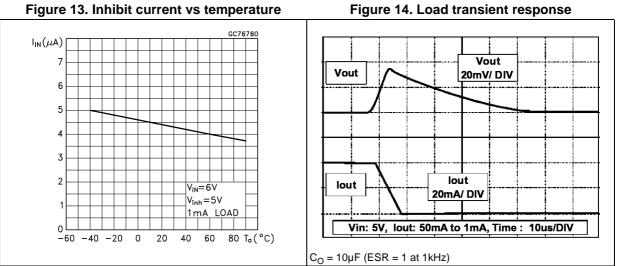
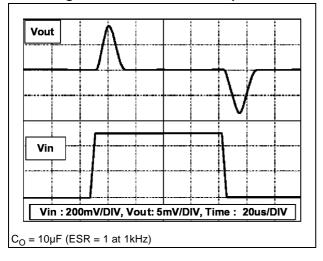


Figure 15. Line transient response





6 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

Dim.	mm		
	Min.	Тур.	Max.
A	0.90		1.45
A1	0		0.15
A2	0.90		1.30
b	0.30		0.50
С	2.09		0.20
D		2.95	
E		1.60	
e		0.95	
н		2.80	
L	0.30		0.60
θ	0		8

Table 5. SOT23-5L mechanical data



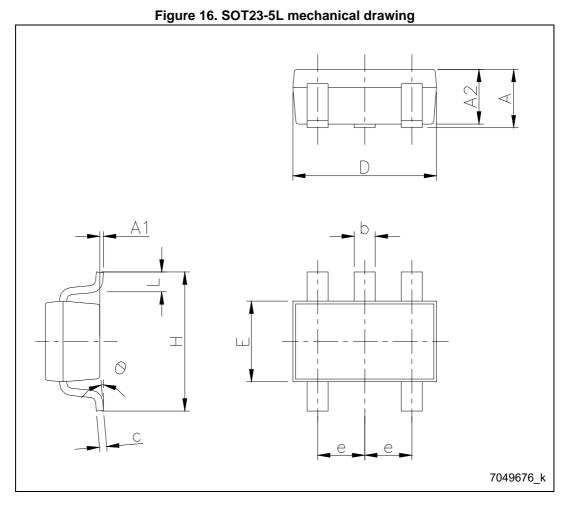
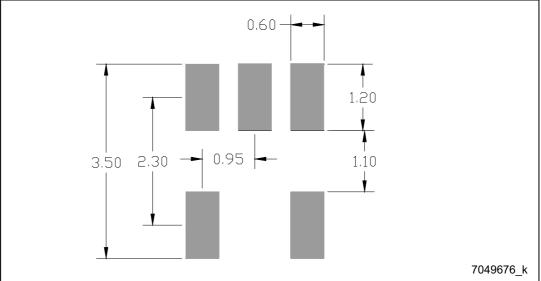


Figure 17. SOT23-5L recommended footprint (dimensions in mm)





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7 Packaging mechanical data

Dim.	ble 6. Tape and reel SC	mm	
	Min.	Тур.	Max.
А			180
С	12.8	13.0	13.2
D	20.2		
Ν	60		
Т			14.4
Ao	3.13	3.23	3.33
Во	3.07	3.17	3.27
Ko	1.27	1.37	1.47
Po	3.9	4.0	4.1
Р	3.9	4.0	4.1



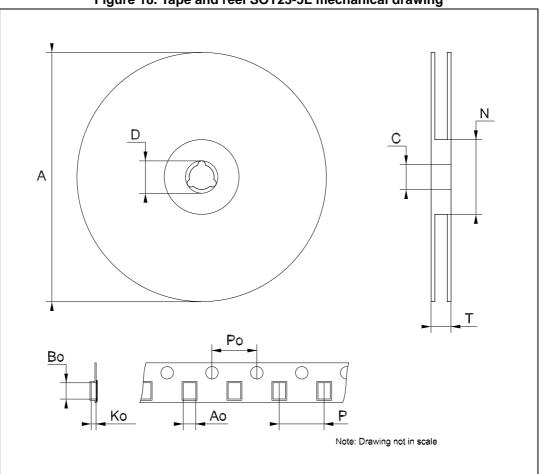


Figure 18. Tape and reel SOT23-5L mechanical drawing



8 Revision history

Date	Revision	Changes	
15-Mar-2005	10	Add tape & reel for TO-92.	
03-Jul-2006	11	Order codes updated.	
16-May-2007	12	Order codes updated.	
08-Jun-2007	13	Order codes updated.	
09-Apr-2008	14	Modified: Table 1 on page 1.	
06-Nov-2013	15	5 Document name changed from LD2979xx to LD2979. Updated <i>Table 1: Device summary, Table 1: Device summary</i> and <i>Section Table 4.: Electrical characteristics.</i> Minor text changes in features and description in cover page.	

Table 7. Document revision history



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