



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



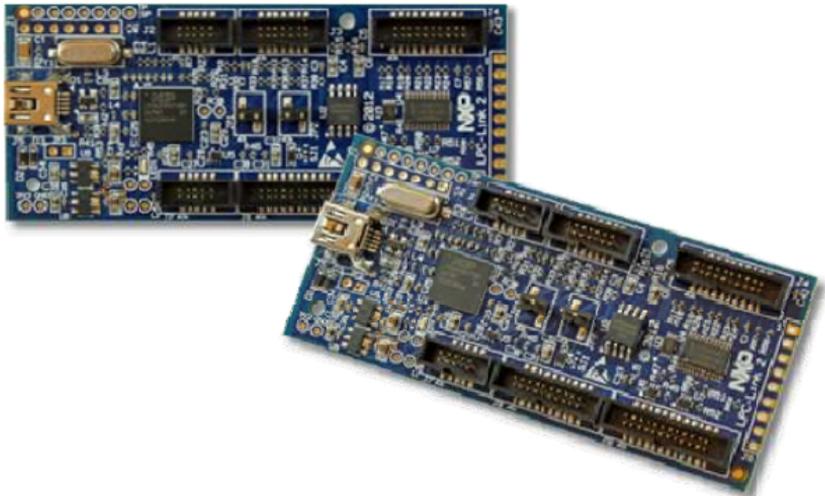
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## LPC-LINK 2

**LPC-Link 2** is an extensible, stand-alone debug adapter that can be configured to support various development tools and IDEs by downloadable firmwares.

Available firmware images include:

- J-Link by Segger
- CMSIS-DAP by ARM
- Code Red Technologies Redlink

The board is supported by the **LPCxpresso IDE** and all development environments that supports the J-Link and CMSIS-DAP interface.

This product is delivered with a **10-pos IDC Ribbon Cable**.

LPC-Link 2 is the latest addition to the LPCxpresso development platform.

**LPC-Link 2**

<i>SWD/JTAG interface voltage</i>	1.2V - 5.5V supported
<i>Dimensions</i>	84 x 37 mm
<i>Powering</i>	Via USB cable from PC
<i>Connectors</i>	<ul style="list-style-type: none"><li>• standard 10-pin JTAG/SWD connector</li><li>• 20-pin JTAG/SWD/ETM connector</li><li>• analog expansion header*</li><li>• digital expansion header*</li><li>• serial expansion header*</li></ul>
<i>Supported development tools</i>	<ul style="list-style-type: none"><li>• Compatible with the <b>LPCXpresso IDE</b> and Code Red Technologies Red Suite via the Redlink interface</li><li>• Compatible with tools/IDEs that support the Segger J-Link via downloadable firmware image</li><li>• Compatible with tools/IDEs that support CMSIS-DAP by ARM via downloadable firmware image</li></ul>

\* Note that schematic and BSP for the board is not yet published. Exactly when this will happen is unknown.