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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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# Si3443DVPbF

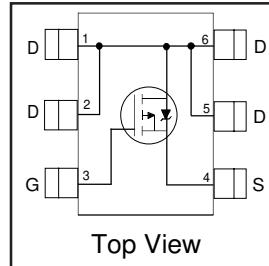
HEXFET® Power MOSFET

- Ultra Low On-Resistance
- P-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- -2.5V Rated
- Lead-Free

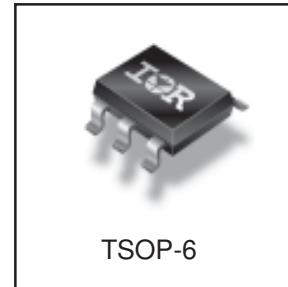
## Description

These P-channel MOSFETs from International Rectifier utilize advanced processing techniques to achieve the extremely low on-resistance per silicon area. This benefit provides the designer with an extremely efficient device for use in battery and load management applications.

The TSOP-6 package with its customized leadframe produces a HEXFET® power MOSFET with  $R_{DS(on)}$  60% less than a similar size SOT-23. This package is ideal for applications where printed circuit board space is at a premium. It's unique thermal design and  $R_{DS(on)}$  reduction enables a current-handling increase of nearly 300% compared to the SOT-23.



$V_{DSS} = -20V$   
 $R_{DS(on)} = 0.065\Omega$



## Absolute Maximum Ratings

	Parameter	Max.	Units
$V_{DS}$	Drain- Source Voltage	-20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-4.4	
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V$	-3.5	A
$I_{DM}$	Pulsed Drain Current $\textcircled{O}$	-20	
$P_D @ T_A = 25^\circ C$	Power Dissipation	2.0	
$P_D @ T_A = 70^\circ C$	Power Dissipation	1.3	W
	Linear Derating Factor	0.016	W/ $^\circ C$
$E_{AS}$	Single Pulse Avalanche Energy $\textcircled{D}$	31	mJ
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to + 150	$^\circ C$

## Thermal Resistance

	Parameter	Max.	Units
$R_{QJA}$	Maximum Junction-to-Ambient $\textcircled{S}$	62.5	$^\circ C/W$

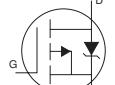
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International  
Rectifier

## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

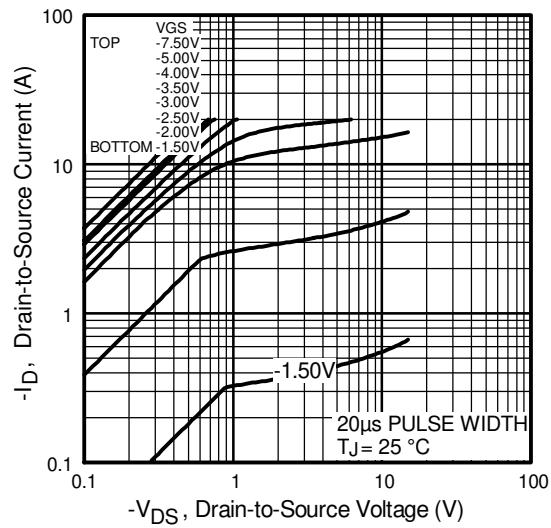
	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	-20	—	—	V	$V_{\text{GS}} = 0\text{V}$ , $I_D = -250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}/\Delta T_J}$	Breakdown Voltage Temp. Coefficient	—	-0.005	—	$\text{V}^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = -1\text{mA}$
$R_{\text{DS}(\text{on})}$	Static Drain-to-Source On-Resistance	—	0.034	0.065	$\Omega$	$V_{\text{GS}} = -4.5\text{V}$ , $I_D = -4.4\text{A}$ ②
		—	0.053	0.090		$V_{\text{GS}} = -2.7\text{V}$ , $I_D = -3.7\text{A}$ ②
		—	0.060	0.100		$V_{\text{GS}} = -2.5\text{V}$ , $I_D = -3.5\text{A}$ ②
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	-0.60	—	-1.2	V	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = -250\mu\text{A}$
$g_f$	Forward Transconductance	—	12	—	S	$V_{\text{DS}} = -10\text{V}$ , $I_D = -4.4\text{ A}$
$I_{\text{DSS}}$	Drain-to-Source Leakage Current	—	—	-1.0	$\mu\text{A}$	$V_{\text{DS}} = -20\text{V}$ , $V_{\text{GS}} = 0\text{V}$
		—	—	-5.0		$V_{\text{DS}} = -20\text{V}$ , $V_{\text{GS}} = 0\text{V}$ , $T_J = 70^\circ\text{C}$
$I_{\text{GSS}}$	Gate-to-Source Forward Leakage	—	—	-100	nA	$V_{\text{GS}} = -12\text{V}$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{\text{GS}} = 12\text{V}$
$Q_g$	Total Gate Charge	—	11	15	nC	$I_D = -4.4\text{A}$
$Q_{\text{gs}}$	Gate-to-Source Charge	—	2.2	—		$V_{\text{DS}} = -10\text{V}$
$Q_{\text{gd}}$	Gate-to-Drain ("Miller") Charge	—	2.9	—		$V_{\text{GS}} = -4.5\text{V}$ ②
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	12	50	ns	$V_{\text{DD}} = -10\text{V}$ , $V_{\text{GS}} = -4.5\text{V}$ ②
$t_r$	Rise Time	—	33	60		$I_D = -1.0\text{A}$
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	70	100		$R_G = 6.0\ \Omega$
$t_f$	Fall Time	—	72	100	pF	$R_D = 10\ \Omega$ , ②
$C_{\text{iss}}$	Input Capacitance	—	1079	—		$V_{\text{GS}} = 0\text{V}$
$C_{\text{oss}}$	Output Capacitance	—	220	—		$V_{\text{DS}} = -10\text{V}$
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	152	—		$f = 1.0\text{MHz}$

## Source-Drain Ratings and Characteristics

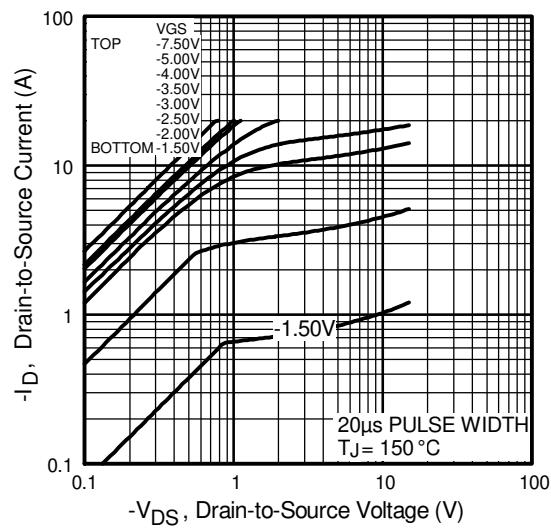
	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	-2.0	A	MOSFET symbol showing the integral reverse p-n junction diode.
$I_{\text{SM}}$	Pulsed Source Current (Body Diode) ①	—	—	-20		
$V_{\text{SD}}$	Diode Forward Voltage	—	—	-1.2	V	$T_J = 25^\circ\text{C}$ , $I_S = -1.7\text{A}$ , $V_{\text{GS}} = 0\text{V}$ ②
$t_{\text{rr}}$	Reverse Recovery Time	—	51	77	ns	$T_J = 25^\circ\text{C}$ , $I_F = -1.7\text{A}$
$Q_{\text{rr}}$	Reverse Recovery Charge	—	30	44	nC	$dI/dt = -100\text{A}/\mu\text{s}$ ②

### Notes:

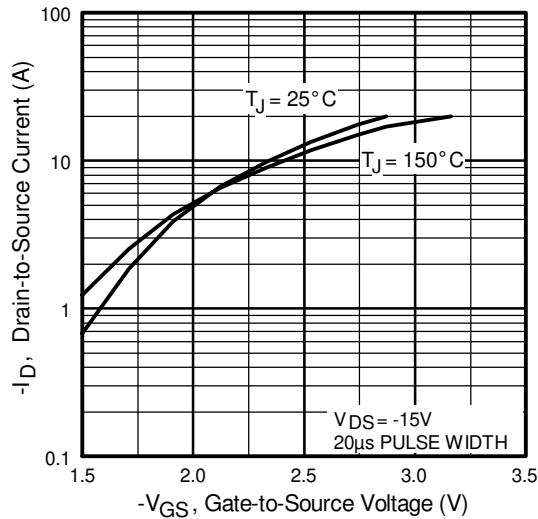
- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ③ Surface mounted on FR-4 board,  $t \leq 5\text{sec}$ .
- ④ Starting  $T_J = 25^\circ\text{C}$ ,  $L = 6.8\text{mH}$   
 $R_G = 25\Omega$ ,  $I_{AS} = -3.0\text{A}$ .



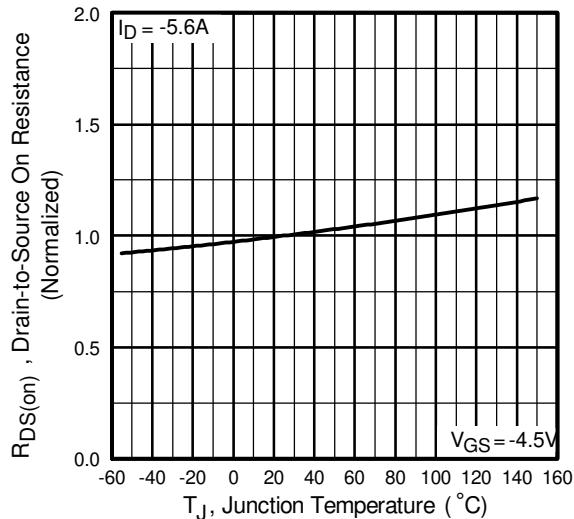
**Fig 1.** Typical Output Characteristics



**Fig 2.** Typical Output Characteristics



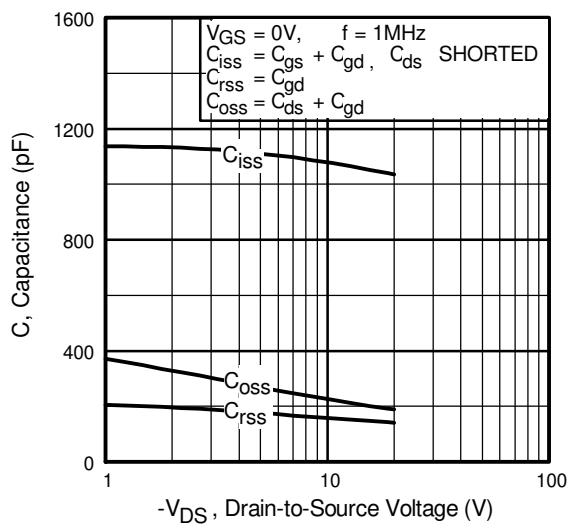
**Fig 3.** Typical Transfer Characteristics



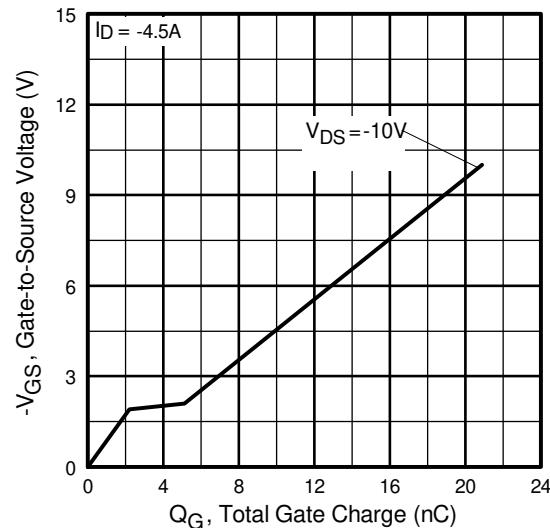
**Fig 4.** Normalized On-Resistance  
Vs. Temperature

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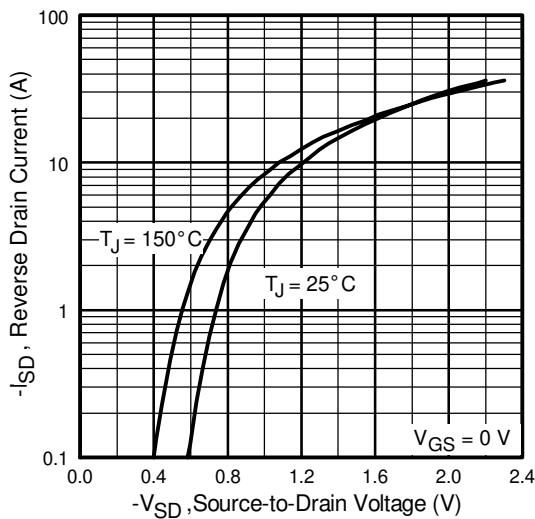
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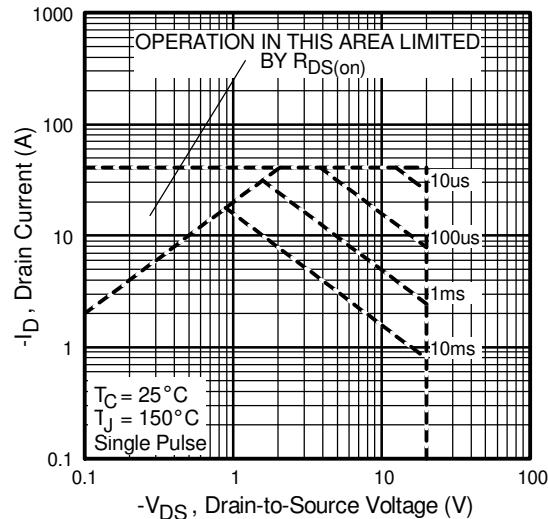
**Fig 5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



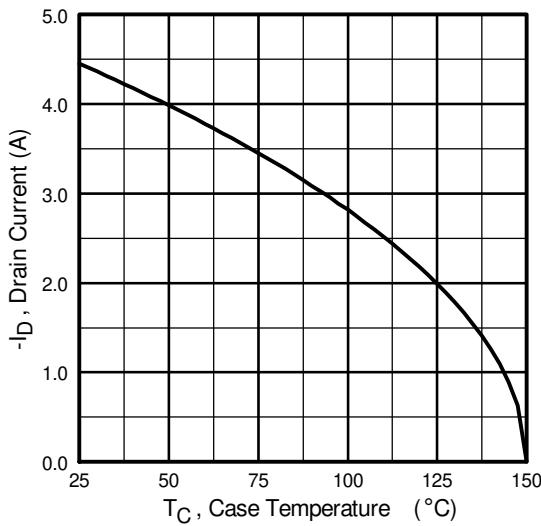
**Fig 6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



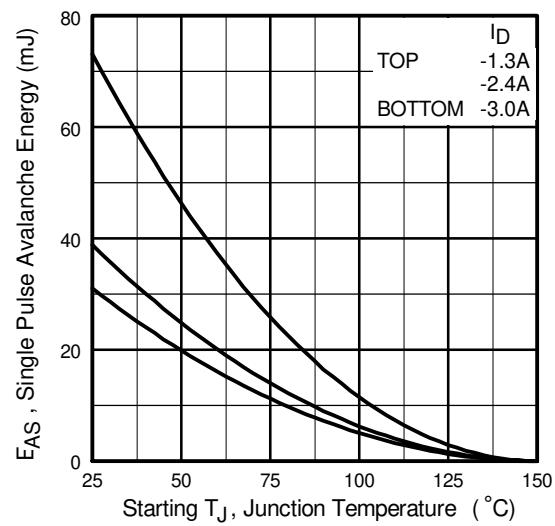
**Fig 7.** Typical Source-Drain Diode  
Forward Voltage



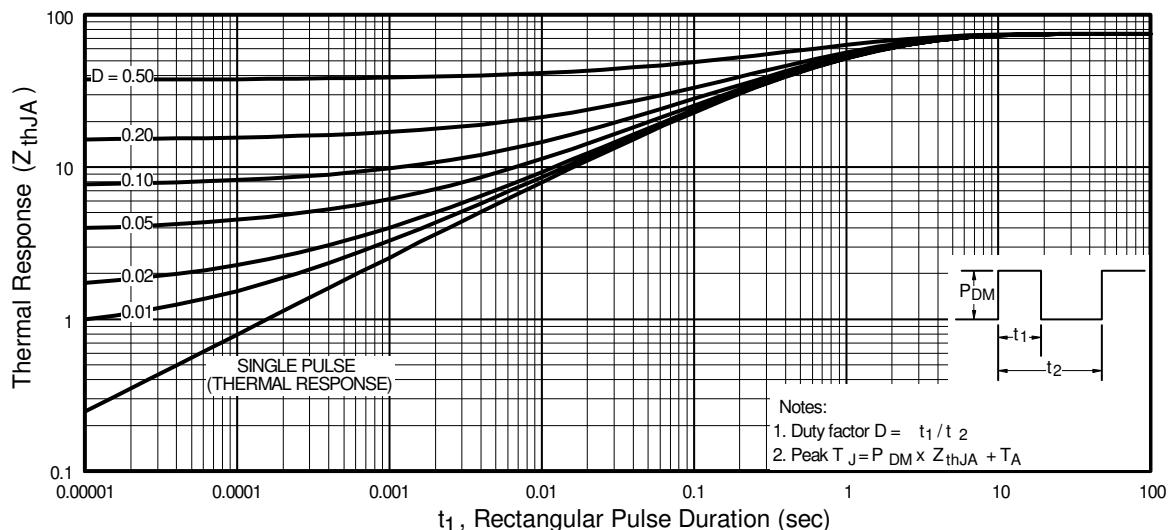
**Fig 8.** Maximum Safe Operating Area



**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10.** Maximum Avalanche Energy  
Vs. Drain Current

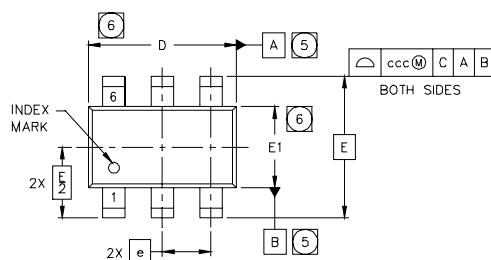


**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

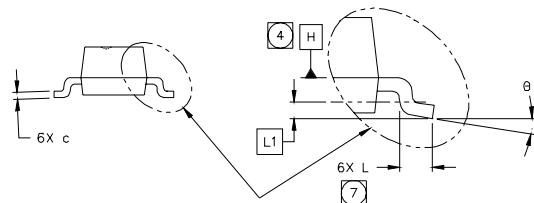
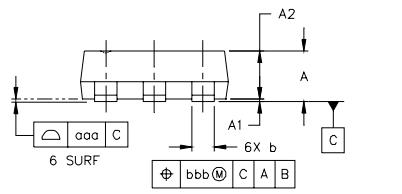
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## TSOP-6 Package Outline

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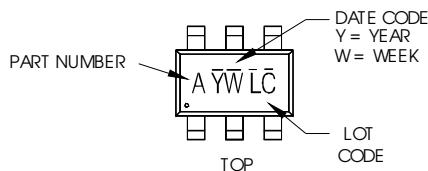


SYMBOL	MO-193AA DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	---	---	1.10	---	---	.0433
A1	0.01	---	0.10	.0004	---	.0039
A2	0.80	0.90	1.00	.0315	.0354	.0393
b	0.25	---	0.50	.0099	---	.0196
c	0.10	---	0.26	.004	---	.010
D	2.90	3.00	3.10	.115	.118	.122
E	2.75	BSC		.108	BSC	
E1	1.30	1.50	1.70	.052	.059	.066
e	1.00	BSC		.039	BSC	
L	0.20	0.40	0.60	.0079	.0157	.0236
L1	0.30	BSC		.0118	BSC	
0	0°	---	8°	0°	---	8°
aaa		0.10			.004	
bbb		0.15			.006	
ccc		0.25			.010	



### TSOP-6 Part Marking Information

W = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR



PART NUMBER CODE REFERENCE:

A = Si3443DV

B = IRF5800

C = IRF5850

D = IRF5851

E = IRF5852

F = IRF5801

I = IRF5805

J = IRF5806

K = IRF5810

L = IRF5804

M = IRF5803

N = IRF5802

YEAR	Y	WORK WEEK	W
2001	1	01	A
2002	2	02	B
2003	3	03	C
2004	4	04	D
2005	5		
2006	6		
2007	7		
2008	8		
2009	9		
2010	0	24	X
		25	Y
		26	Z

W = (27-52) IF PRECEDED BY A LETTER

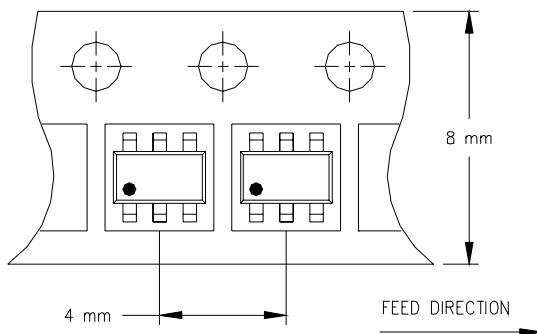
YEAR	Y	WORK WEEK	W
2001	A	27	A
2002	B	28	B
2003	C	29	C
2004	D	30	D
2005	E		
2006	F		
2007	G		
2008	H		
2009	J		
2010	K	50	X
		51	Y
		52	Z

Note: A line above the work week (as shown here) indicates Lead-Free.

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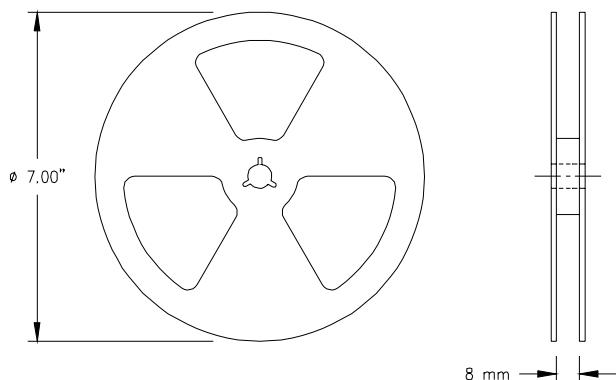
## TSOP-6 Tape & Reel Information

**Si3443DVPbF**



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.  
This product has been designed and qualified for the Consumer market.  
Qualifications Standards can be found on IR's Web site.

International  
**IR** Rectifier

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