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CMOS Digital Integrated Circuits Silicon Monolithic

# TC7WH00FU

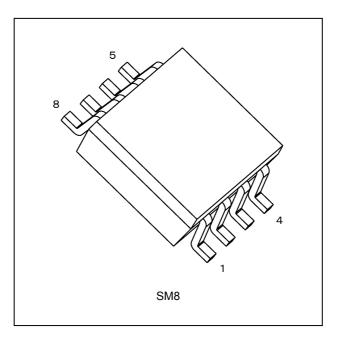
### 1. Functional Description

• Dual 2-Input NAND Gate

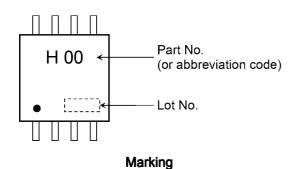
### 2. Features

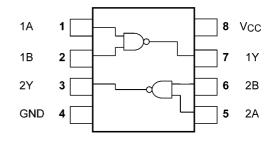
- (1) Wide operating temperature range:  $T_{opr} = -40$  to 85 °C
- (2) High speed operation:  $t_{pd}$  = 3.7 ns (typ.) (V<sub>CC</sub> = 5.0 V, C<sub>L</sub> = 15 pF)
- (3) Low power dissipation:  $I_{CC} = 2.0 \ \mu A \ (max) \ (T_a = 25 \ ^\circ C)$
- (4) High noise immunity:  $V_{NIH} = V_{NIL} = 28 \% V_{CC}$  (min)
- (5) 5.5 V tolerant inputs
- (6) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (7) Wide operating voltage range:  $V_{CC}$  = 2.0 to 5.5 V
- (8) Identical pin assignment and function with TC7W00

### 3. Packaging



4. Marking and Pin Assignment





Pin Assignment (Top view)

Start of commercial production 1997-10 2017-04-20 Rev.3.0

## TOSHIBA

### 5. IEC Logic Symbol



### 6. Truth Table

| А | В | Y |
|---|---|---|
| L | L | Н |
| L | Н | Н |
| н | L | Н |
| н | Н | L |

### 7. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

| Characteristics                 | Symbol           | Note     | Rating                        | Unit |
|---------------------------------|------------------|----------|-------------------------------|------|
| Supply voltage                  | V <sub>CC</sub>  |          | -0.5 to 7.0                   | V    |
| Input voltage                   | V <sub>IN</sub>  |          | -0.5 to 7.0                   |      |
| DC output voltage               | V <sub>OUT</sub> |          | -0.5 to V <sub>CC</sub> + 0.5 |      |
| Input diode current             | I <sub>IK</sub>  |          | -20                           | mA   |
| Output diode current            | I <sub>OK</sub>  | (Note 1) | ±20                           | 1    |
| DC output current               | I <sub>OUT</sub> |          | ±25                           |      |
| V <sub>CC</sub> /ground current | I <sub>CC</sub>  |          | ±50                           |      |
| Power dissipation               | PD               |          | 300                           | mW   |
| Storage temperature             | T <sub>stg</sub> |          | -65 to 150                    | °C   |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{OUT}$  < GND,  $V_{OUT}$  >  $V_{CC}$ 

### 8. Operating Ranges (Note)

| Characteristics          | Symbol           | Test Condition         | Rating               | Unit |
|--------------------------|------------------|------------------------|----------------------|------|
| Supply voltage           | V <sub>CC</sub>  | _                      | 2.0 to 5.5           | V    |
| Input voltage            | V <sub>IN</sub>  |                        | 0 to 5.5             |      |
| Output voltage           | V <sub>OUT</sub> | _                      | 0 to V <sub>CC</sub> |      |
| Operating temperature    | T <sub>opr</sub> | _                      | -40 to 85            | °C   |
| Input rise and fall time | dt/dv            | $V_{CC}$ = 3.3 ± 0.3 V | 0 to 100             | ns/V |
|                          |                  | $V_{CC}$ = 5.0 ± 0.5 V | 0 to 20              |      |

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

## TOSHIBA

### 9. Electrical Characteristics

### 9.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

| Characteristics           | Symbol          | Test Condition  | 1                       | V <sub>CC</sub> (V) | Min                 | Тур. | Max                 | Unit |
|---------------------------|-----------------|---|-------------------------|---------------------|---------------------|------|---------------------|------|
| High-level input voltage  | V <sub>IH</sub> | —   |                         | 2.0                 | 1.5                 | _    | —                   | V    |
|                           |                 |   |                         | 3.0 to 5.5          | $V_{CC} \times 0.7$ | _    | —                   |      |
| Low-level input voltage   | V <sub>IL</sub> | —   |                         | 2.0                 | —                   | _    | 0.5                 | V    |
|                           |                 |   |                         | 3.0 to 5.5          | —                   | -    | $V_{CC} \times 0.3$ |      |
| High-level output voltage | V <sub>OH</sub> | $V_{\rm IN} = V_{\rm IH} \text{ or } V_{\rm IL}$ $I_{\rm OH} = -50 \ \mu$ |                         | 2.0                 | 1.9                 | 2.0  | —                   | V    |
|                           |                 |   |                         | 3.0                 | 2.9                 | 3.0  | —                   |      |
|                           |                 |   |                         | 4.5                 | 4.4                 | 4.5  | —                   |      |
|                           |                 |   | I <sub>OH</sub> = -4 mA | 3.0                 | 2.58                |      | —                   |      |
|                           |                 |   | I <sub>OH</sub> = -8 mA | 4.5                 | 3.94                |      | —                   |      |
| Low-level output voltage  | V <sub>OL</sub> | V <sub>IN</sub> = V <sub>IH</sub>   | I <sub>OL</sub> = 50 μA | 2.0                 | —                   | 0.0  | 0.1                 | V    |
|                           |                 |   |                         | 3.0                 | —                   | 0.0  | 0.1                 |      |
|                           |                 |   |                         | 4.5                 | —                   | 0.0  | 0.1                 |      |
|                           |                 |   | I <sub>OL</sub> = 4 mA  | 3.0                 | —                   | _    | 0.36                |      |
|                           |                 |   | I <sub>OL</sub> = 8 mA  | 4.5                 |                     | _    | 0.36                |      |
| Input leakage current     | I <sub>IN</sub> | V <sub>IN</sub> = 5.5 V or GND  |                         | 0 to 5.5            | —                   | _    | ±0.1                | μA   |
| Quiescent supply current  | I <sub>CC</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND                                  |                         | 5.5                 |                     |      | 2.0                 | μA   |

### 9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

| Characteristics           | Symbol          | Test Con                                 | V <sub>CC</sub> (V)      | Min        | Max                 | Unit                |    |
|---------------------------|-----------------|--|--------------------------|------------|---------------------|---------------------|----|
| High-level input voltage  | VIH             | —  |                          | 2.0        | 1.5                 | —                   | V  |
|                           |                 |  |                          | 3.0 to 5.5 | $V_{CC} \times 0.7$ | _                   |    |
| Low-level input voltage   | VIL             | _  |                          | 2.0        | _                   | 0.5                 | V  |
|                           |                 |  |                          | 3.0 to 5.5 | _                   | $V_{CC} \times 0.3$ |    |
| High-level output voltage | V <sub>OH</sub> | $V_{IN} = V_{IH} \text{ or } V_{IL}$     | I <sub>OH</sub> = -50 μA | 2.0        | 1.9                 | —                   | V  |
|                           |                 |  |                          | 3.0        | 2.9                 | —                   |    |
|                           |                 |  |                          | 4.5        | 4.4                 | _                   |    |
|                           |                 |  | I <sub>OH</sub> = -4 mA  | 3.0        | 2.48                | _                   |    |
|                           |                 |  | I <sub>OH</sub> = -8 mA  | 4.5        | 3.80                | _                   |    |
| Low-level output voltage  | V <sub>OL</sub> | V <sub>IN</sub> = V <sub>IH</sub>        | I <sub>OL</sub> = 50 μA  | 2.0        | _                   | 0.1                 | V  |
|                           |                 |  |                          | 3.0        | _                   | 0.1                 |    |
|                           |                 |  |                          | 4.5        | _                   | 0.1                 |    |
|                           |                 |  | I <sub>OL</sub> = 4 mA   | 3.0        | _                   | 0.44                |    |
|                           |                 |  | I <sub>OL</sub> = 8 mA   | 4.5        | _                   | 0.44                |    |
| Input leakage current     | I <sub>IN</sub> | V <sub>IN</sub> = 5.5 V or GND           | ·                        | 0 to 5.5   | _                   | ±1.0                | μA |
| Quiescent supply current  | I <sub>CC</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND |                          | 5.5        | _                   | 20.0                | μA |

### 9.3. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

| Characteristics                  | Symbol                             | Note     | Test<br>Condition | V <sub>CC</sub> (V) | C <sub>L</sub> (pF) | Min | Тур. | Max  | Unit |
|----------------------------------|------------------------------------|----------|-------------------|---------------------|---------------------|-----|------|------|------|
| Propagation delay time           | t <sub>PLH</sub> ,t <sub>PHL</sub> |          | _                 | $3.3\pm0.3$         | 15                  | _   | 5.5  | 7.9  | ns   |
|                                  |                                    |          |                   |                     | 50                  | _   | 8.0  | 11.4 |      |
|                                  |                                    |          |                   | $5.0\pm0.5$         | 15                  | -   | 3.7  | 5.5  |      |
|                                  |                                    |          |                   |                     | 50                  |     | 5.2  | 7.5  |      |
| Input capacitance                | C <sub>IN</sub>                    |          | _                 |                     |                     | _   | 4    | 10   | pF   |
| Power dissipation<br>capacitance | C <sub>PD</sub>                    | (Note 1) | —                 |                     |                     | _   | 19   | _    | pF   |

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2 \text{ (per 1 gate)}$ 

#### 9.4. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

| Characteristics        | Symbol                             | Test Condition | V <sub>CC</sub> (V)           | C <sub>L</sub> (pF) | Min | Max  | Unit |
|------------------------|------------------------------------|----------------|-------------------------------|---------------------|-----|------|------|
| Propagation delay time | t <sub>PLH</sub> ,t <sub>PHL</sub> | —              | $\textbf{3.3}\pm\textbf{0.3}$ | 15                  | 1.0 | 9.5  | ns   |
|                        |                                    |                |                               | 50                  | 1.0 | 13.0 |      |
|                        |                                    |                | $5.0\pm0.5$                   | 15                  | 1.0 | 6.5  |      |
|                        |                                    |                |                               | 50                  | 1.0 | 8.5  |      |
| Input capacitance      | C <sub>IN</sub>                    | _              |                               |                     | _   | 10   | pF   |

### 9.5. Noise Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

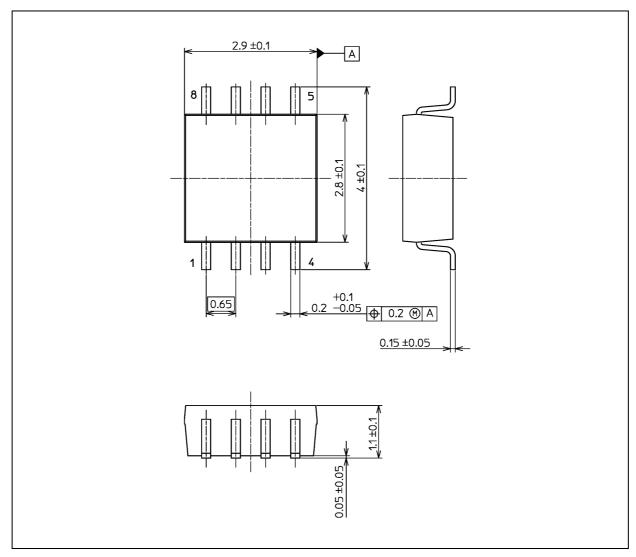
| Characteristics                              | Symbol           | Test Condition         | V <sub>CC</sub> (V) | Тур. | Limit | Unit |
|--|------------------|------------------------|---------------------|------|-------|------|
| Quiet output maximum dynamic V <sub>OL</sub> | V <sub>OLP</sub> | C <sub>L</sub> = 50 pF | 5.0                 | 0.3  | 0.8   | V    |
| Quiet output minimum dynamic V <sub>OL</sub> | V <sub>OLV</sub> | C <sub>L</sub> = 50 pF | 5.0                 | -0.3 | -0.8  | V    |
| Minimum high-level dynamic input voltage     | V <sub>IHD</sub> | C <sub>L</sub> = 50 pF | 5.0                 | _    | 3.5   | V    |
| Maximum low-level dynamic input voltage      | V <sub>ILD</sub> | C <sub>L</sub> = 50 pF | 5.0                 |      | 1.5   | V    |



### TC7WH00FU

### Package Dimensions

Unit: mm



Weight: 21 mg (typ.)

|                | Package Name(s) |  |
|----------------|-----------------|--|
| JEDEC: SOT-505 |                 |  |
| Nickname: SM8  |                 |  |

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