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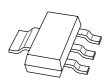
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Kind regards,

Team Nexperia



PBHV8115Z150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistorRev. 02 - 9 December 2008Product data sheet

1. Product profile

1.1 General description

NPN high-voltage low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBHV9115Z.

1.2 Features

- High voltage
- Low collector-emitter saturation voltage V_{CEsat}
- \blacksquare High collector current capability I_C and I_{CM}
- High collector current gain (h_{FE}) at high I_C
- AEC-Q101 qualified
- Medium power SMD plastic package

1.3 Applications

- LED driver for LED chain module
- LCD backlighting
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	150	V
I _C	collector current		-	-	1	А
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 50 mA	100	250	-	



150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	collector		2, 4
3	emitter		1
4	collector		ר 3
			sym016

3. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PBHV8115Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223			

4. Marking

Table 4.	Marking codes	
Type num	ıber	Marking code
PBHV811	5Z	V8115Z

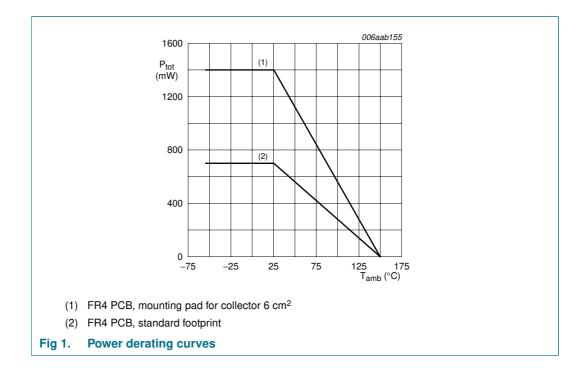
150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

5. Limiting values

Table 5. In accordar	Limiting values ace with the Absolute Maximu	m Rating System (IE	C 60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	400	V
V _{CEO}	collector-emitter voltage	open base	-	150	V
V _{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	2	A
I _{BM}	peak base current	single pulse; $t_p \leq 1 ms$	-	400	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1] -	0.7	W
			[2] _	1.4	W
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



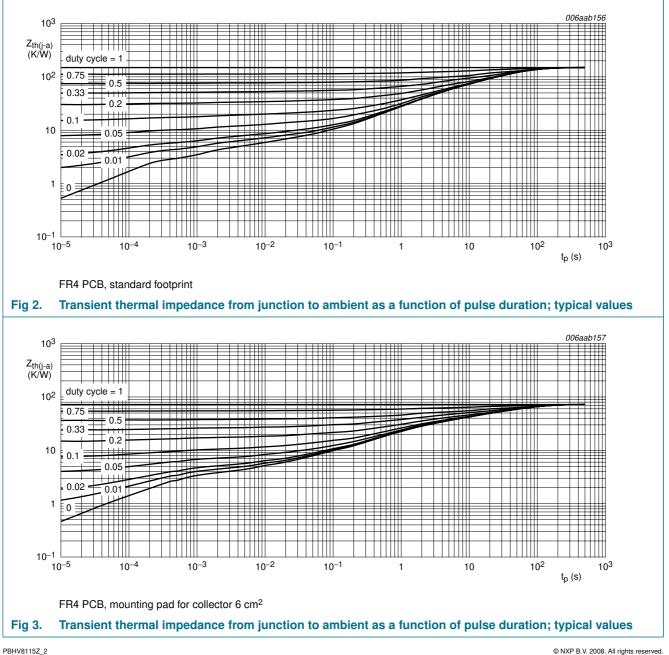
150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Mi	n Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	<u>[1]</u> _	-	175	K/W
	junction to ambient		[2] _	-	89	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm².



150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

7. Characteristics

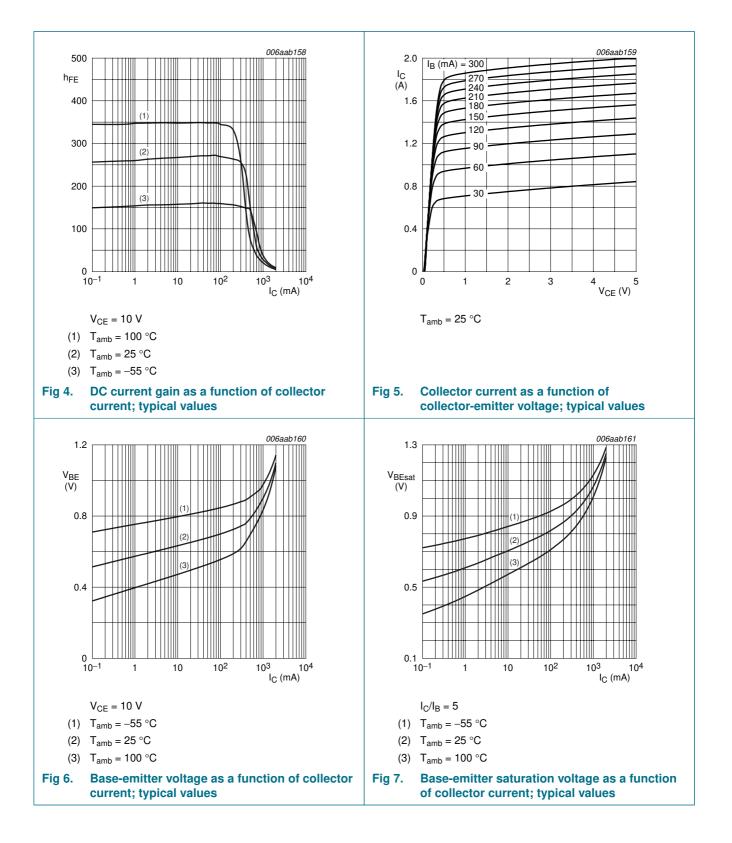
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	$V_{CB} = 120 \text{ V}; \text{ I}_{E} = 0 \text{ A}$		-	-	100	nA
	current	$V_{CB} = 120 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 \text{ °C}$		-	-	10	μA
I _{CES}	collector-emitter cut-off current	$V_{CE} = 120 \text{ V}; \text{ V}_{BE} = 0 \text{ V}$		-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 4 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 10 V					
		I _C = 50 mA		100	250	-	
		I _C = 100 mA		100	250	-	
		I _C = 0.5 A	[1]	50	160	-	
		I _C = 1 A	[1]	10	30	-	
V _{CEsat}	collector-emitter	$I_{C} = 100 \text{ mA}; I_{B} = 10 \text{ mA}$		-	40	60	mV
	saturation voltage	$I_{C} = 100 \text{ mA}; I_{B} = 20 \text{ mA}$		-	33	50	mV
		$I_{C} = 1 \text{ A}; I_{B} = 200 \text{ mA}$	<u>[1]</u>	-	225	350	mV
V _{BEsat}	base-emitter saturation voltage	$I_{C} = 1 \text{ A}; I_{B} = 200 \text{ mA}$	<u>[1]</u>	-	1.1	1.2	V
f _T	transition frequency	V _{CE} = 10 V; I _E = 10 mA; f = 100 MHz		-	30	-	MH
C _c	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB} = 20 \ V; \ I_E = i_e = 0 \ A; \\ f = 1 \ MHz \end{array}$		-	5.7	-	pF
C _e	emitter capacitance	$\label{eq:Veb} \begin{array}{l} V_{EB}=0.5 \text{ V}; \text{ I}_{C}=\text{i}_{c}=0 \text{ A};\\ \text{f}=1 \text{ MHz} \end{array}$		-	150	-	pF
t _d	delay time	$V_{CC} = 6 \text{ V}; I_{C} = 0.5 \text{ A};$		-	7	-	ns
t _r	rise time	$I_{Bon} = 0.1 \text{ A}; I_{Boff} = -0.1 \text{ A}$		-	565	-	ns
t _{on}	turn-on time			-	572	-	ns
t _s	storage time			-	1530	-	ns
t _f	fall time			-	700	-	ns
t _{off}	turn-off time			-	2230	-	ns

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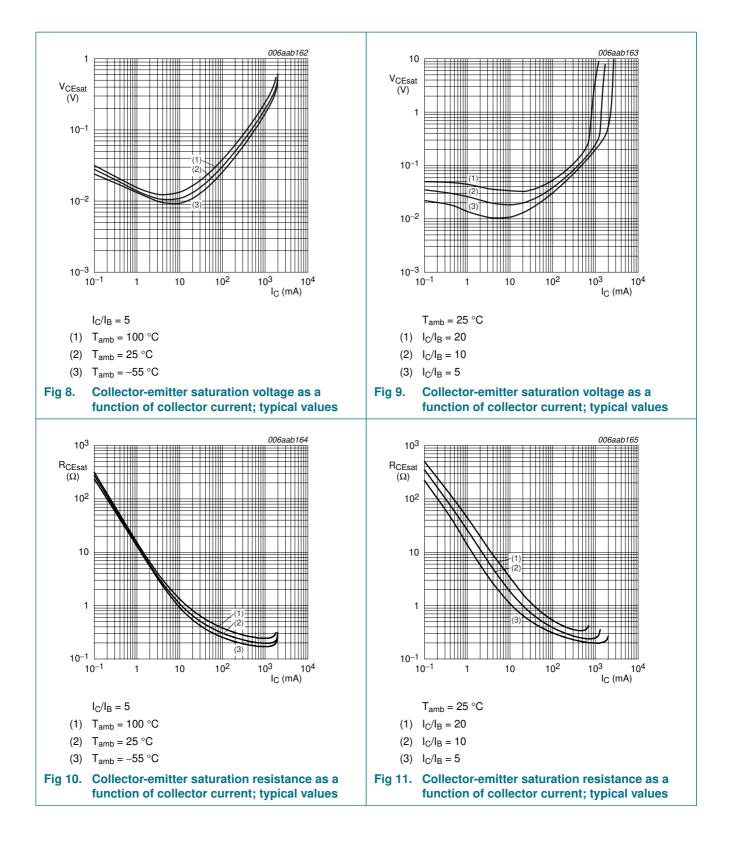
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PBHV8115Z

150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

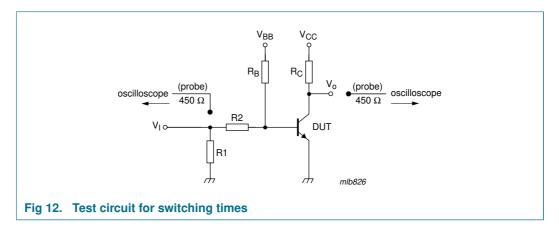


150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor



150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

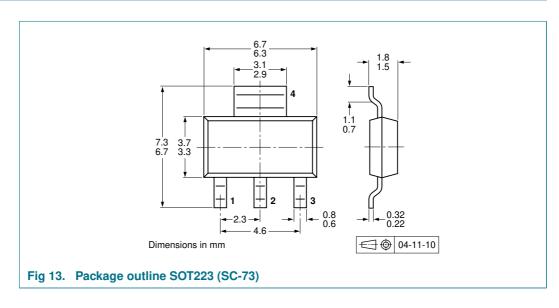
8. Test information



8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

Table 8. Packing methods

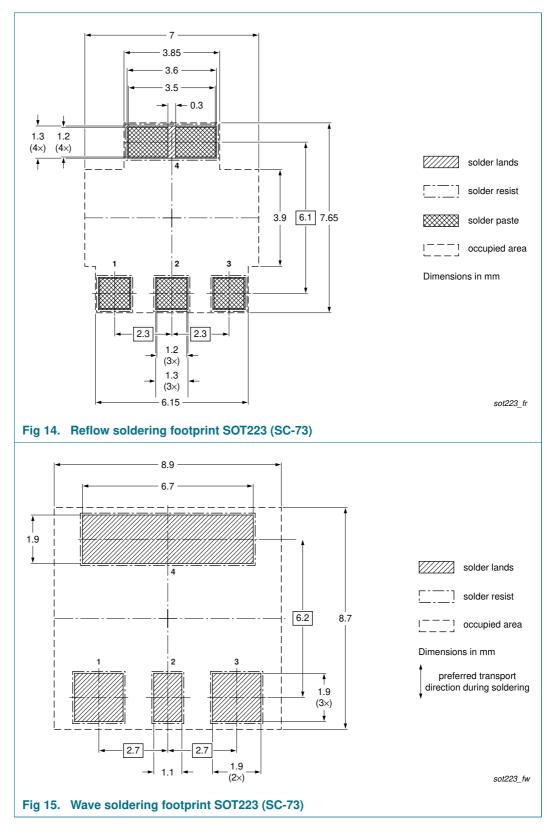
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing	quantity
			1000	4000
PBHV8115Z	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

11. Soldering



150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

12. Revision history

Table 9. Revision h	istory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PBHV8115Z_2	20081209	Product data sheet	-	PBHV8115Z_1
Modifications:	• Figure 5: an			
	Section 13	Legal information": update	a	
PBHV8115Z_1	20080205	Product data sheet	-	-

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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PBHV8115Z

150 V, 1 A NPN high-voltage low V_{CEsat} (BISS) transistor

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Date of release: 9 December 2008 Document identifier: PBHV8115Z_2