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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCT74AF, TC74VHCT74AFT

Dual D-Type Flip-Flop with Preset and Clear

The TC74VHCT74 is an advanced high speed CMOS D-TYPE FLIP –FLOP fabricated with silicon gate C^2MOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The signal level applied to the D INPUT is transferred to Q OUTPUT during the positive going transition of the CK pulse.

CLR and PR are independent of the CK and are accomplished by setting the appropriate input low.

The input voltage are compatible with TTL output voltage.

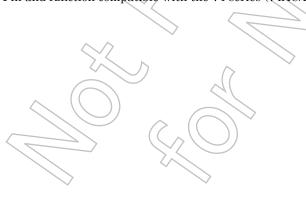
This device may be used as a level converter for interfacing $3.3\ V$ to $5\ V$ system.

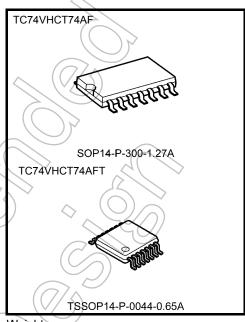
Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output ^(Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

Note: $V_{CC} = 0 V$

Features

- High speed: $f_{max} = 160 \text{ MHz}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 2 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- Compatible with TTL inputs: $V_{IL} = 0.8 \text{ V (max)}$ $V_{IH} = 2.0 \text{ V (min)}$
- · Power down protection is provided on all inputs and outputs
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 74 type.

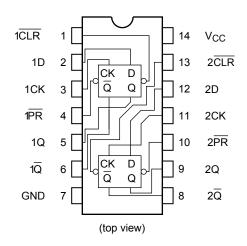




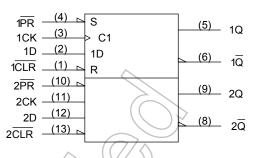
Weight

SOP14-P-300-1.27A: 0.18 g (typ.) TSSOP14-P-0044-0.65A: 0.06 g (typ.)

Pin Assignment



IEC Logic Symbol



Truth Table

	Inp	uts		Out	puts	Function	
CLR	PR	D	CK	Q	Q	i unction	
L	Н	Х	Х	L	Н	Clear	
Н	L	Х	Х	Н	L	Preset	
L	L	Χ	Х	Н	Н	- (
Н	Η	Ш		Ь	Н	74	
Н	Н	Н		Н	L	4	
Н	Н	Х		Qn	\overline{Q}_n	No Change	

X: Don't care

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit		
Supply voltage range	// Ŷcc	-0.5 to 7.0		V	
DC input voltage	V _{IN}	-0.5 to 7.0		V	
DC output voltage	Vout	-0.5 to 7.0	(Note 2)	V	
DC output voltage	VOU1	-0.5 to V _{CC} + 0.5	(Note 3)	· ·	
Input diode current	lıĸ	-20		mA	
Output diode current	lok	±20	(Note 4)	mA	
DC output current	lout	±25		mA	
DC V _{CC} /ground current	Ico	±50		mA	
Power dissipation	PD	180		mW	
Storage temperature	T _{stg}	−65 to 150		°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0 V$

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: V_{OUT} < GND, V_{OUT} > V_{CC}

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	٧
Output voltage	V _{OUT}	0 to 5.5 (Note 2)	\ \
Output voltage	V001	0 to V _{CC} (Note 3)	
Operating temperature	T _{opr}	-40 to 85	·c)
Input rise and fall time	dt/dV	0 to 20	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: $V_{CC} = 0 V$ Note 3: High or low state

Electrical Characteristics

DC Characteristics

								/	
Characteristics	Symbol	Test Condition Ta = 25°C Ta = -40 to 85°C						Unit	
	,	~(V _{CC} (V)	Min	Typ.	Max	Min	Max	
High-level input voltage	V _{IH}	- (4.5 to 5.5	2.0		_ (2.0	ı	٧
Low-level input voltage	V _{IL}	_d()	4.5 to 5.5		$))_{\perp}$	0.8	ı	0.8	>
High-level output	V _{OH}	V _{IN}	4.5	4.40	4.50	_	4.40	1	V
voltage	VOH	= V _{IH} or V _{IL}	4.5	3.94	_	_	3.80	_	٧
Low-level output	V _{OL}	V _{IN} (1 _{OL} = 50 μA	4.5	-	0.0	0.1	1	0.1	V
voltage		= V _{IH} or V _{IL} I _{OL} = 8 mA	4.5		ı	0.36	ı	0.44	٧
Input leakage current	IIN	V _{IN} = 5.5 V or GND	0 to 5.5	_	ı	±0.1	-	±1.0	μΑ
Outroport supply	//Icc	$V_{IN} = V_{CC}$ or GND	5.5	1	-	2.0	ı	20.0	μΑ
Quiescent supply current	loct	Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND	5.5	-	1	1.35	-	1.50	mA
Output leakage current	lopp	V _{OUT} = 5.5 V	0	_	_	0.5	_	5.0	μΑ

3



Timing Requirements (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition	Ta = 25°C	Ta = -40 to 85°C	Unit		
			V _{CC} (V)	Limit	Limit	nit	
Minimum pulse width (CK)	t _{w (L)}	_	5.0 ± 0.5	5.0	5.0	ns	
Minimum pulse width (CLR , PR)	t _{w (L)}	_	5.0 ± 0.5	5.0	5.0	ns	
Minimum set-up time	t _s	- /	5.0 ± 0.5	5.0	5.0	ns	
Minimum hold time	t _h	-	5.0 ± 0.5	0.0	0.0	ns	
Minimum removal time ($\overline{\text{CLR}}$, $\overline{\text{PR}}$)	t _{rem}	-	5.0 ± 0.5	3.5	3.5	ns	

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition Ta = 25°C						Ta = -40 to 85°C		Unit
	- J		V _{CC} (V)	C _L (pF)	Min	Typ.	Max	Min	Max	
Propagation delay time	t _{pLH}	_	5.0 ± 0.5	15	. –	5.8	7.8	1.0	9.0	ns
(CK-Q, \overline{Q})	t _{pHL}		0.0104	50	_	6.3	8.8	1.0	10.0	110
Propagation delay time	t _{pLH}	_	5.0 ± 0.5	15	_	7.6	10.4	1.0	12.0	ns
$(\overline{CLR},\;\overline{PR}-Q,\;\overline{Q})$	t _{pHL}	_	0.010.5	50	_	8.1	11.4	1.0	13.0	113
Maximum clock	f		5.0 ± 0.5	15	100	160	_	80	_	MHz
frequency	f _{max}	-((0.0 ± 0.5	50	80	140	_	65	_	IVITIZ
Input capacitance	C _{IN}	(7)	_		_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}		4	(Note)	> -	24	_	_		pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

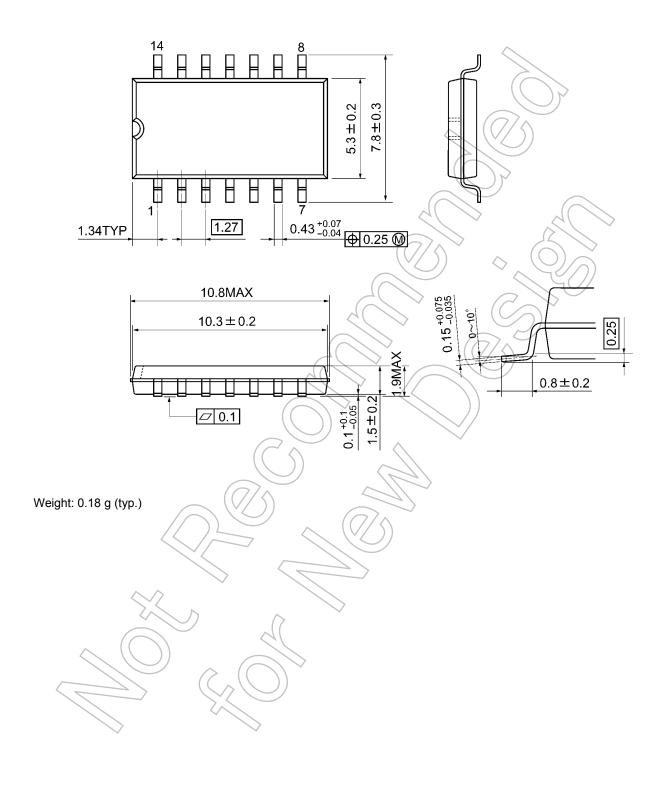
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2 (per F/F)$



Package Dimensions

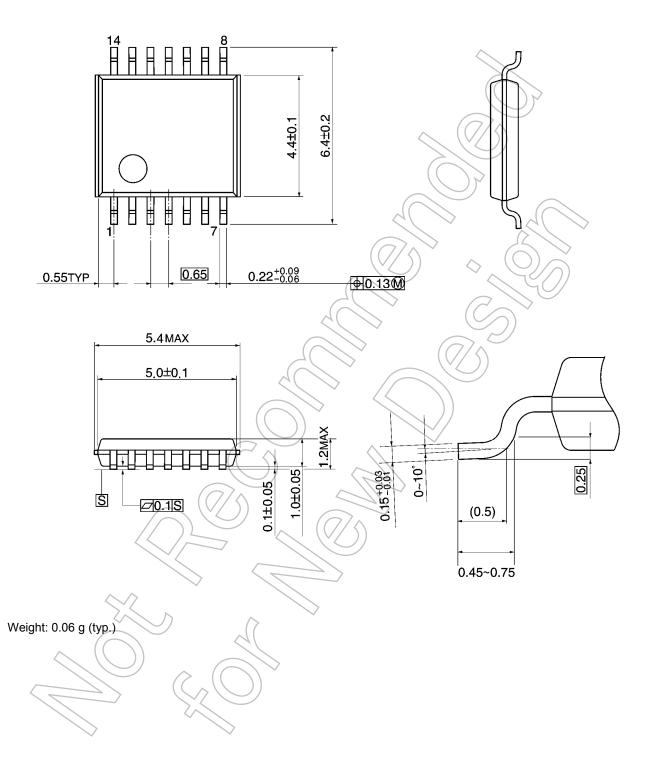
SOP14-P-300-1.27A Unit: mm



5

Package Dimensions

TSSOP14-P-0044-0.65A Unit: mm



6

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