Panasonic

DRC9114W0L

Silicon NPN epitaxial planar type

For digital circuits
DRC5114W in SSMini3 type package

■ Features

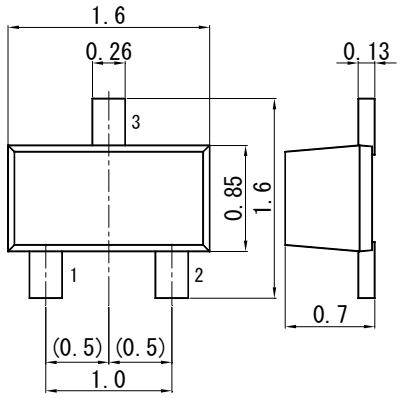
- Low collector-emitter saturation voltage Vce(sat)
- Halogen-free / RoHS compliant
  (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)

■ Marking Symbol: N9

■ Packaging

Embossed type (Thermo-compression sealing) : 3 000 pcs / reel (standard)

Unit: mm



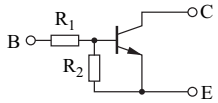
1. Base
2. Emitter
3. Collector

Panasonic	SSMini3-F3-B
JEITA	SC-89
Code	SOT-490

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	VCBO	50	V
Collector-emitter voltage (Base open)	VCEO	50	V
Collector current	IC	100	mA
Total power dissipation	PT	125	mW
Junction temperature	Tj	150	°C
Operating ambient temperature	Topr	-40 to +85	°C
Storage temperature	Tstg	-55 to +150	°C

Internal Connection



Resistance value	R1	10	kΩ
	R2	4.7	kΩ

■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	VCBO	IC = 10 µA, IE = 0	50			V
Collector-emitter voltage (Base open)	VCEO	IC = 2 mA, IB = 0	50			V
Collector-base cutoff current (Emitter open)	ICBO	VCB = 50 V, IE = 0			0.1	µA
Collector-emitter cutoff current (Base open)	ICEO	VCE = 50 V, IB = 0			0.5	µA
Emitter-base cutoff current (Collector open)	IEBO	VEB = 6 V, IC = 0			1	mA
Forward current transfer ratio	hFE	VCE = 10 V, IC = 5 mA	20			-
Collector-emitter saturation voltage	VCE(sat)	IC = 10 mA, IB = 0.5 mA			0.25	V
Input voltage	Vi(on)	VCE = 0.2 V, IC = 5 mA	3.0			V
	Vi(off)	VCE = 5 V, IC = 100 µA			1.3	V
Input resistance	R1		-30%	10	+30%	kΩ
Resistance ratio	R1/R2		1.70	2.13	2.60	-

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

Established : 2009-10-22
Revised : 2014-03-13

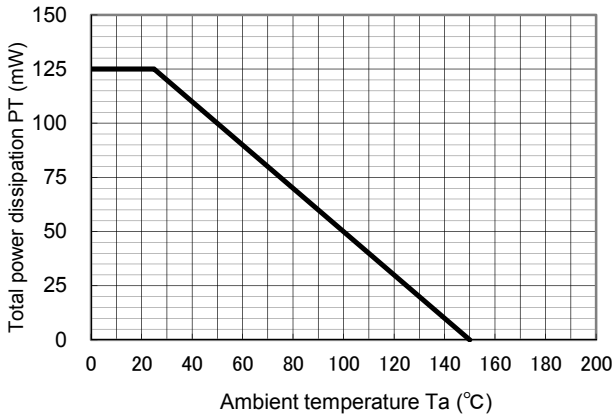
Panasonic

Transistors with Built-in Resistor

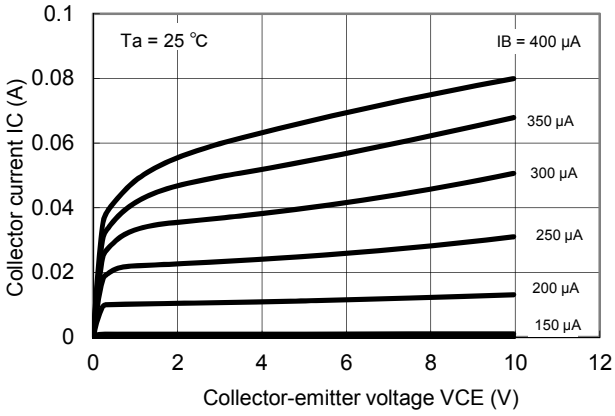
DRC9114W0L

Technical Data (reference)

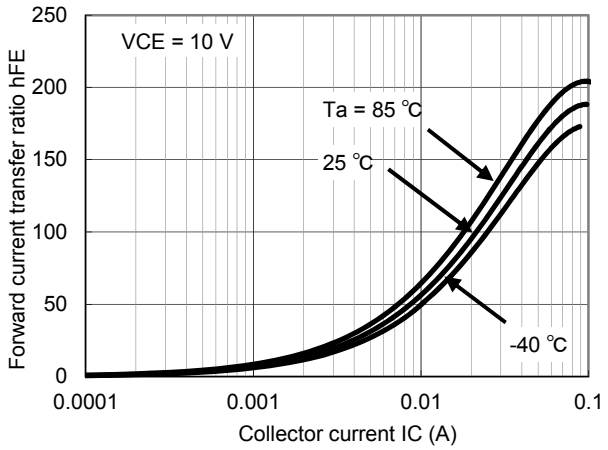
PT - Ta



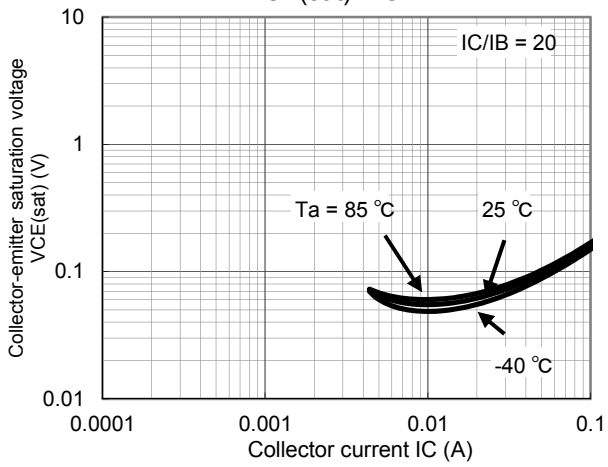
IC - VCE



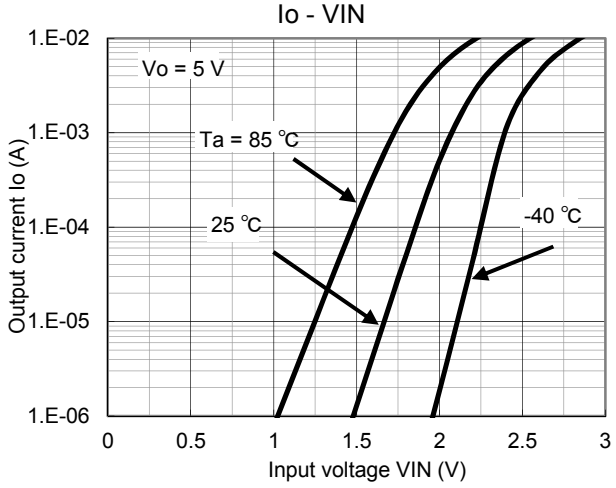
hFE - IC



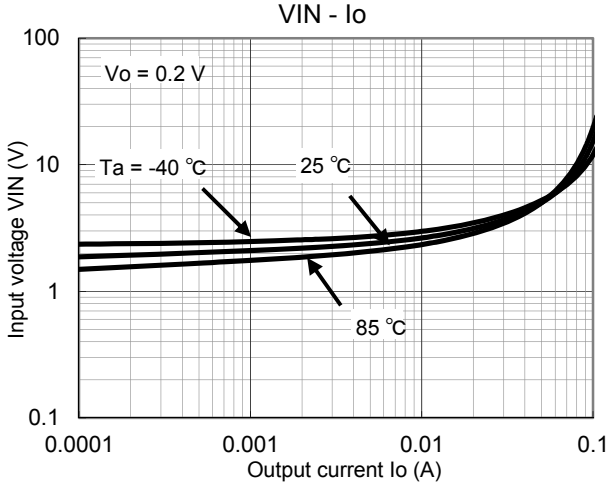
VCE(sat) - IC



Io - VIN



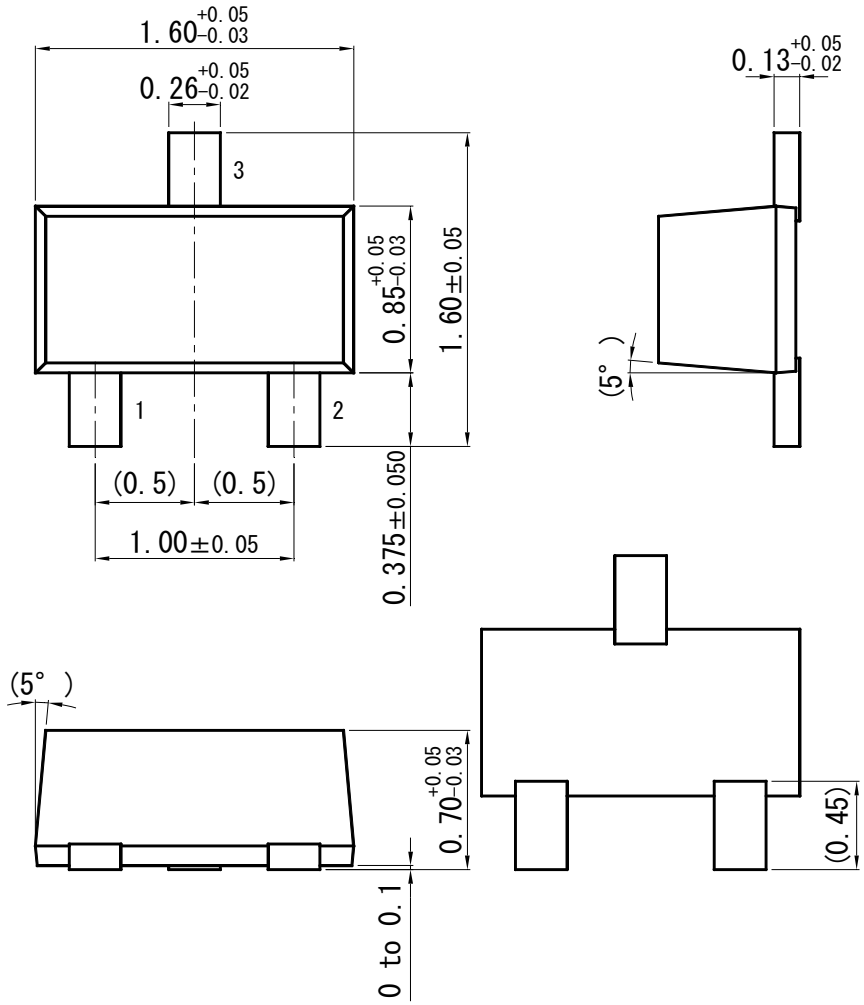
VIN - Io



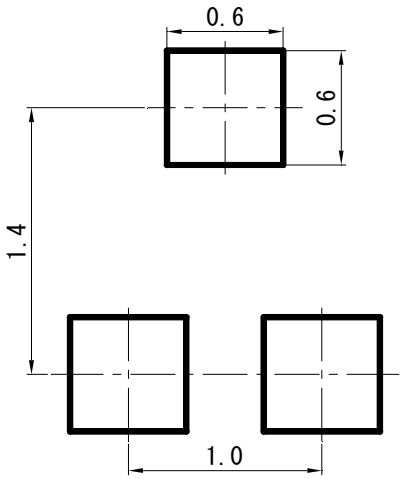
Established : 2009-10-22
Revised : 2014-03-13

SSMini3-F3-B

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



Established : 2009-10-22
Revised : 2014-03-13

Request for your special attention and precautions in using the technical information and semiconductors described in this book

(1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.

(2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information de-scribed in this book.

(3) The products described in this book are intended to be used for general applications (such as office equipment, communications equipment, measuring instruments and household appliances), or for specific applications as expressly stated in this book.
Please consult with our sales staff in advance for information on the following applications, moreover please exchange documents separately on terms of use etc.: Special applications (such as for in-vehicle equipment, airplanes, aerospace, automotive equipment, traffic signaling equipment, combustion equipment, medical equipment and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
Unless exchanging documents on terms of use etc. in advance, it is to be understood that our company shall not be held responsible for any damage incurred as a result of or in connection with your using the products described in this book for any special application.

(4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.

(5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.

(6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. We do not guarantee quality for disassembled products or the product re-mounted after removing from the mounting board.
When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.

(7) When reselling products described in this book to other companies without our permission and receiving any claim of request from the resale destination, please understand that customers will bear the burden.

No.010618