

1. General description

Planar passivated high commutation three quadrant triac in a SOT404 (D2PAK) surface mountable plastic package intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. This "series BT" triac will commutate the full rated RMS current at the maximum rated junction temperature ($T_{j(max)} = 150\text{ °C}$) without the aid of a snubber.

2. Features and benefits

- 3Q technology for improved noise immunity
- High blocking voltage capability
- High junction operating temperature capability ($T_{j(max)} = 150\text{ °C}$)
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by dV/dt
- Less sensitive gate for very high noise immunity
- Planar passivated for voltage ruggedness and reliability
- Surface mountable package
- Triggering in three quadrants only

3. Applications

- Heating controls
- High power motor control
- High power switching
- Applications subject to high temperature ($T_{j(max)} = 150\text{ °C}$)

4. Quick reference data

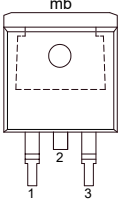

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 117\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	-	25	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig. 4 ; Fig. 5	-	-	190	A
		full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$	-	-	209	A
T_j	junction temperature		-	-	150	°C
Static characteristics						
I_{GT}	gate trigger current	$V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; Fig. 7	2	18	50	mA

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2+ \text{ G-}; T_j = 25\text{ }^\circ\text{C}; \text{ Fig. 7}$	2	21	50	mA
		$V_D = 12\text{ V}; I_T = 0.1\text{ A}; T_2- \text{ G-}; T_j = 25\text{ }^\circ\text{C}; \text{ Fig. 7}$	2	34	50	mA
I_H	holding current	$V_D = 12\text{ V}; T_j = 25\text{ }^\circ\text{C}; \text{ Fig. 9}$	-	31	60	mA
V_T	on-state voltage	$I_T = 30\text{ A}; T_j = 25\text{ }^\circ\text{C}; \text{ Fig. 10}$	-	1.3	1.55	V
Dynamic characteristics						
dV_D/dt	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}; T_j = 150\text{ }^\circ\text{C}; (V_{DM} = 67\% \text{ of } V_{DRM}); \text{ exponential waveform; gate open circuit}$	-	2300	-	V/ μs
		$V_{DM} = 536\text{ V}; T_j = 125\text{ }^\circ\text{C}; \text{ exponential waveform; gate open circuit}$	1000	4000	-	V/ μs
dI_{com}/dt	rate of change of commutating current	$V_D = 400\text{ V}; T_j = 150\text{ }^\circ\text{C}; I_{T(RMS)} = 25\text{ A}; dV_{com}/dt = 20\text{ V}/\mu\text{s}; (\text{without snubber condition}); \text{ gate open circuit}$	-	19	-	A/ms
		$V_D = 400\text{ V}; T_j = 125\text{ }^\circ\text{C}; I_{T(RMS)} = 25\text{ A}; dV_{com}/dt = 20\text{ V}/\mu\text{s}; (\text{snubber condition}); \text{ gate open circuit}$	-	44	-	A/ms

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	 <p>D2PAK (SOT404)</p>	 <p>sym051</p>
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA225B-800BT	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 117\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3	-	25	A
I_{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig. 4 ; Fig. 5	-	190	A
		full sine wave; $T_{j(\text{init})} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$	-	209	A
I^2t	I^2t for fusing	$t_p = 10\text{ ms}$; SIN	-	180	A ² s
di_T/dt	rate of rise of on-state current	$I_G = 100\text{ mA}$	-	100	A/ μ s
I_{GM}	peak gate current		-	2	A
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T_{stg}	storage temperature		-40	150	°C
T_j	junction temperature		-	150	°C

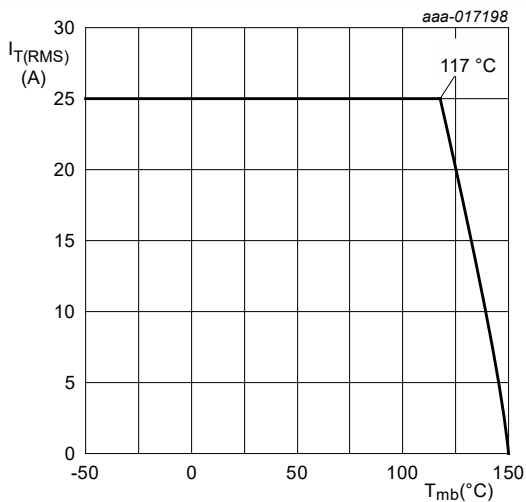


Fig. 1. RMS on-state current as a function of mounting base temperature

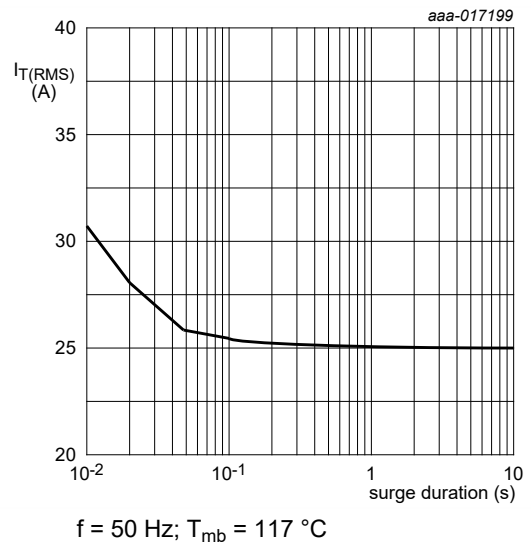


Fig. 2. RMS on-state current as a function of surge duration; maximum values

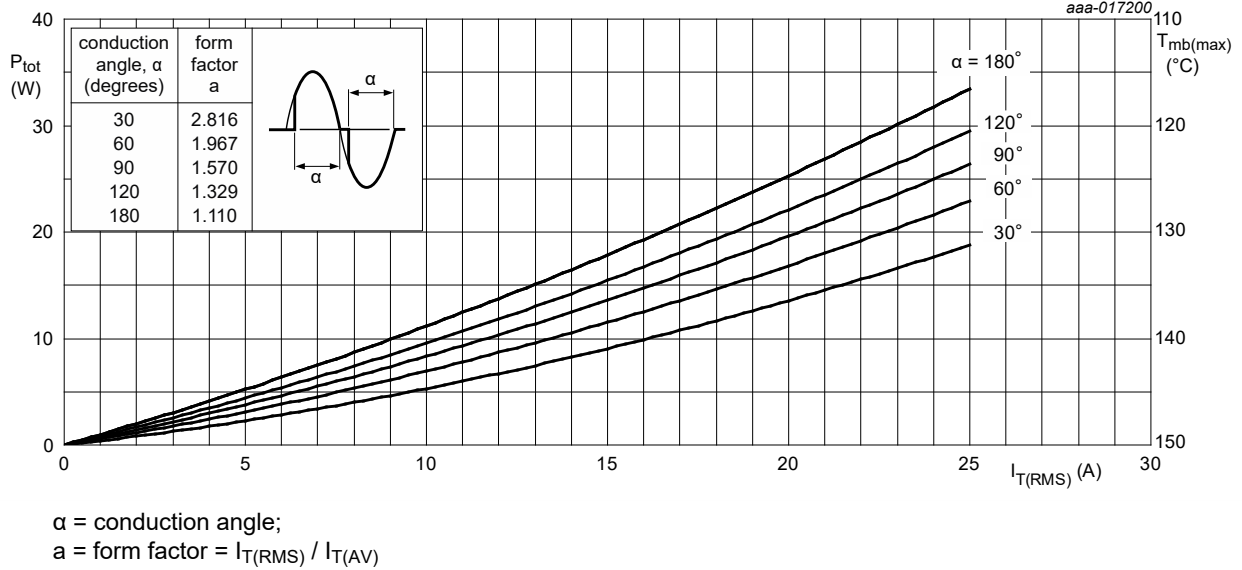


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

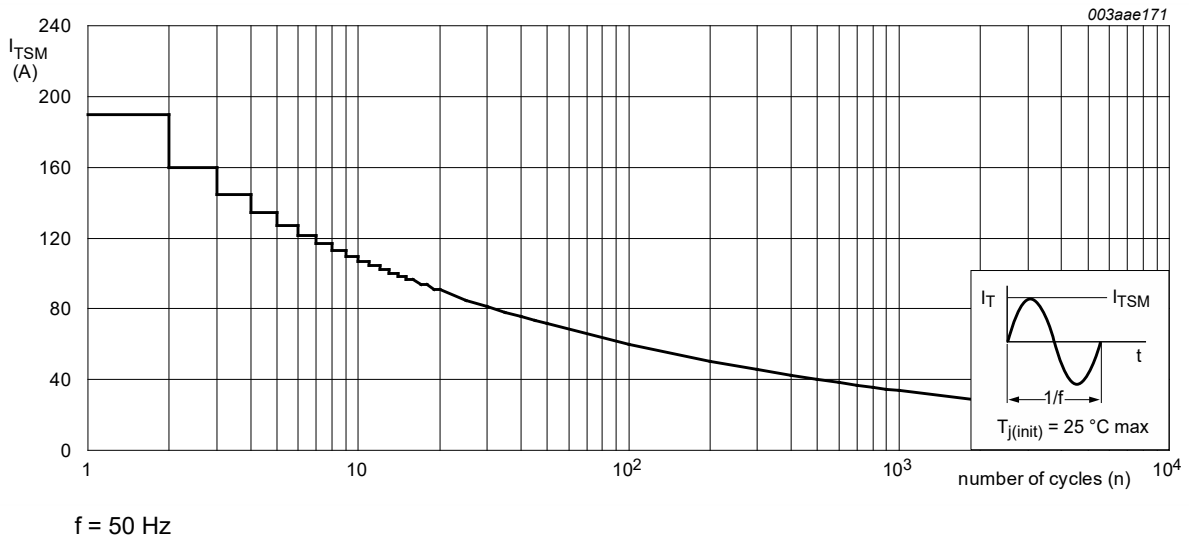


Fig. 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values

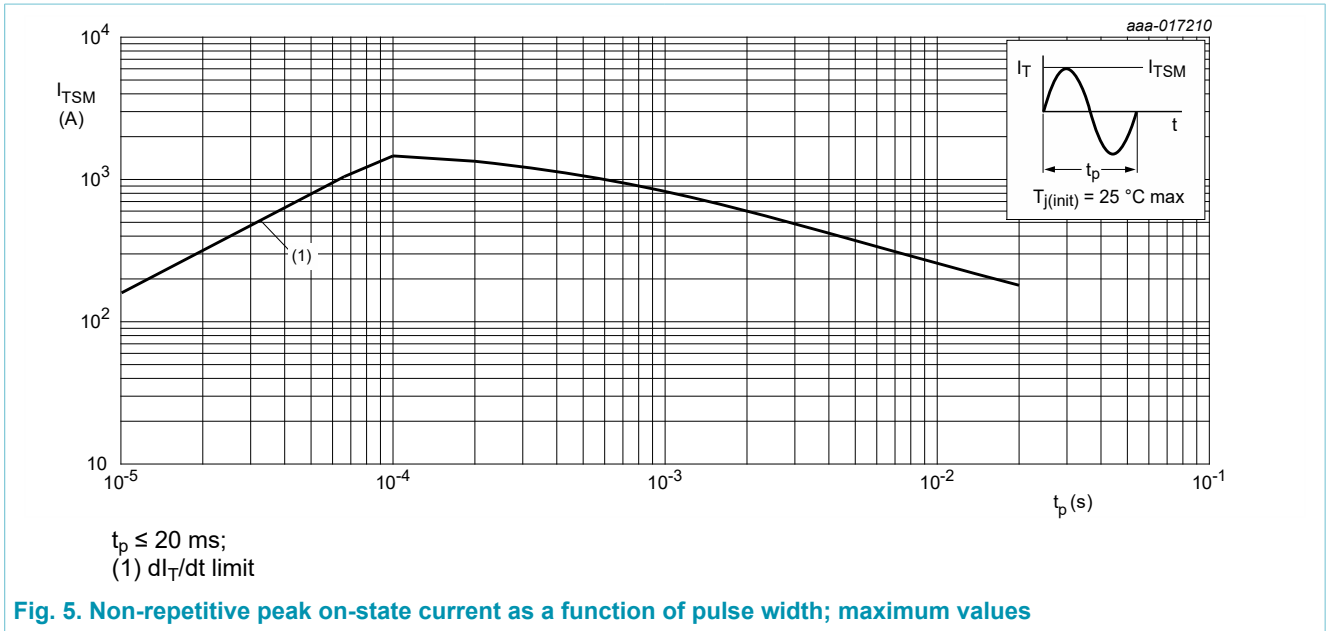
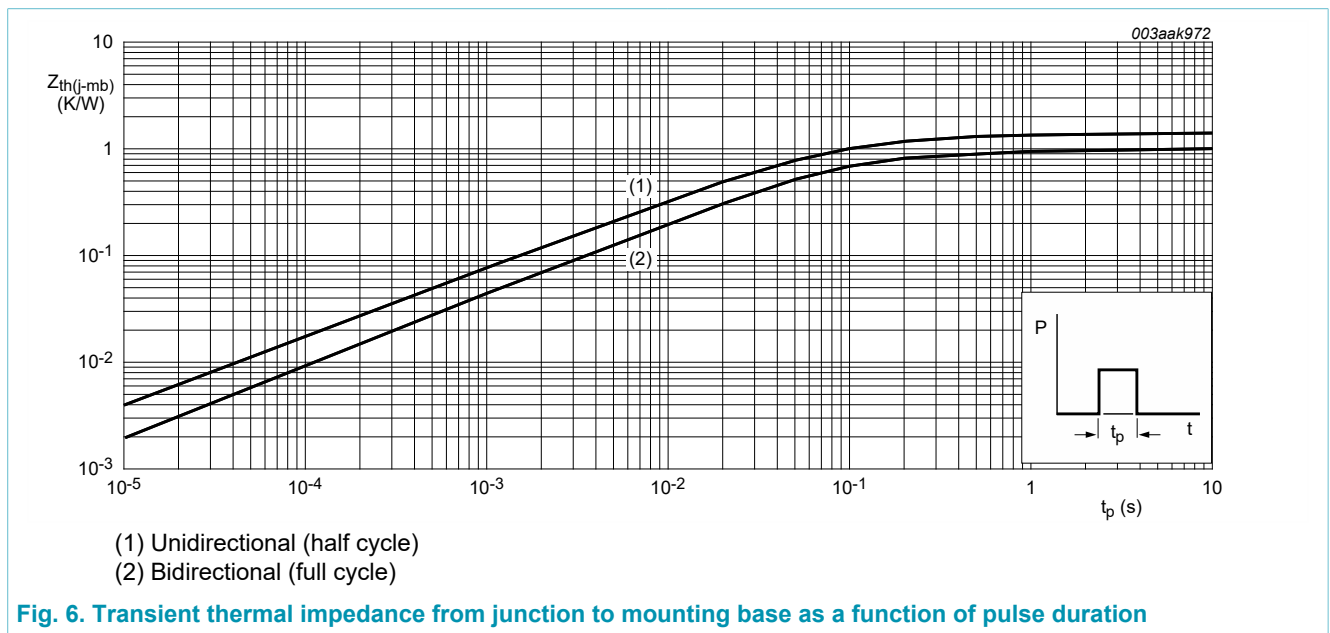


Fig. 5. Non-repetitive peak on-state current as a function of pulse width; maximum values

8. Thermal characteristics

Table 5. Thermal characteristics

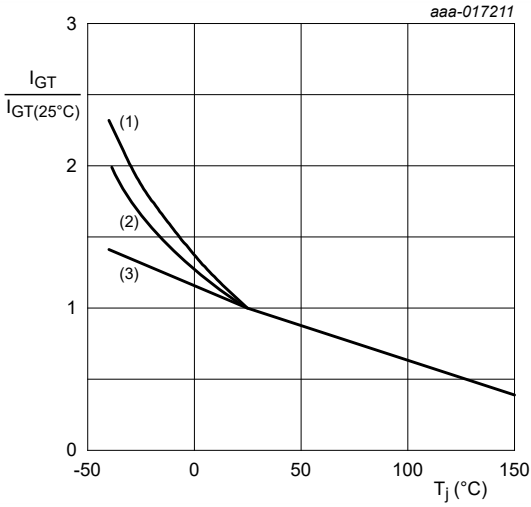
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle; Fig. 6	-	-	1	K/W
		half cycle; Fig. 6	-	-	1.4	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air	printed circuit board (FR4) mounted	-	55	-	K/W



9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 7	2	18	50	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 7	2	21	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; Fig. 7	2	34	50	mA
I _L	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; Fig. 8	-	31	60	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; Fig. 8	-	34	90	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; Fig. 8	-	30	60	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; Fig. 9	-	31	60	mA
V _T	on-state voltage	I _T = 30 A; T _j = 25 °C; Fig. 10	-	1.3	1.55	V
V _{GT}	gate trigger voltage	V _D = 400 V; I _T = 0.1 A; T _j = 150 °C	-	0.6	-	V
		V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; Fig. 11	-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11	0.25	0.4	-	V
I _D	off-state current	V _D = 800 V; T _j = 150 °C	-	0.8	4	mA
		V _D = 800 V; T _j = 25 °C	-	0.1	10	μA
		V _D = 800 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic characteristics						
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 536 V; T _j = 150 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	-	2300	-	V/μs
		V _{DM} = 536 V; T _j = 125 °C; exponential waveform; gate open circuit	1000	4000	-	V/μs
di _{com} /dt	rate of change of commutating current	V _D = 400 V; T _j = 150 °C; I _{T(RMS)} = 25 A; dV _{com} /dt = 20 V/μs; (without snubber condition); gate open circuit	-	19	-	A/ms
		V _D = 400 V; T _j = 125 °C; I _{T(RMS)} = 25 A; dV _{com} /dt = 20 V/μs; (snubber condition); gate open circuit	-	44	-	A/ms



- (1) T2- G-
- (2) T2+ G-
- (3) T2+ G+

Fig. 7. Normalized gate trigger current as a function of junction temperature

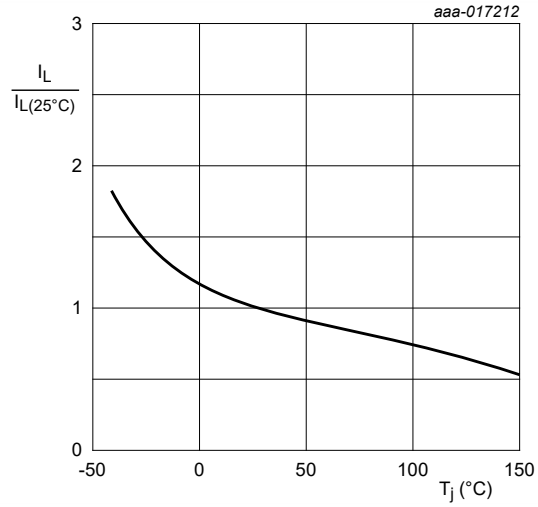


Fig. 8. Normalized latching current as a function of junction temperature

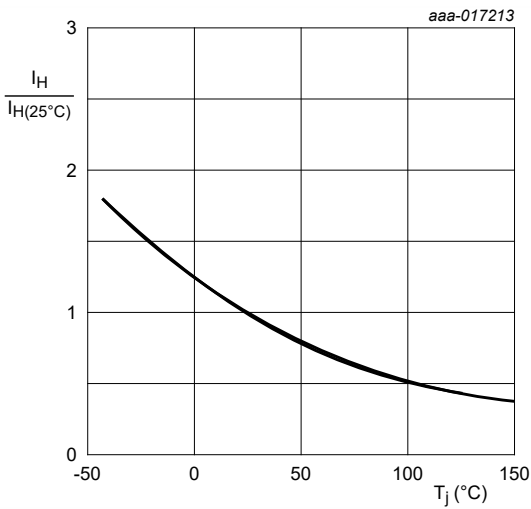
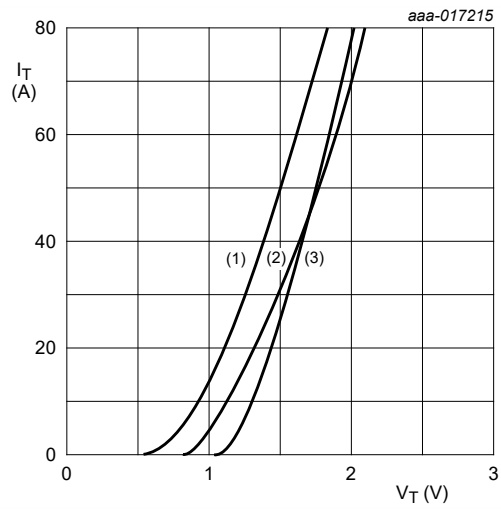


Fig. 9. Normalized holding current as a function of junction temperature



$V_o = 1.061\text{ V}; R_s = 0.015\ \Omega$

- (1) $T_j = 150^\circ\text{C}$; typical values
- (2) $T_j = 150^\circ\text{C}$; maximum values
- (3) $T_j = 25^\circ\text{C}$; maximum values

Fig. 10. On-state current as a function of on-state voltage

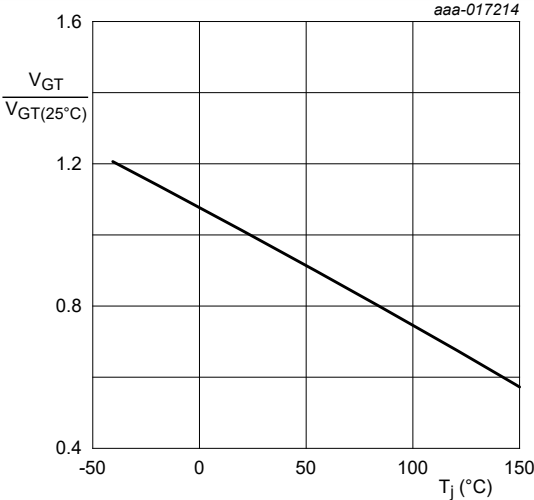


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

10. Package outline



Fig. 12. Package outline D2PAK (SOT404)

11. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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