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**60V N-CHANNEL SELF PROTECTED ENHANCEMENT MODE  
INTELLIFET® MOSFET**

## Product Summary

- Continuous Drain Source Voltage  $V_{DS} = 60V$
- On-State Resistance 500mΩ
- Nominal Load Current ( $V_{IN} = 5V$ ) 1.2A
- Clamping Energy 210mJ

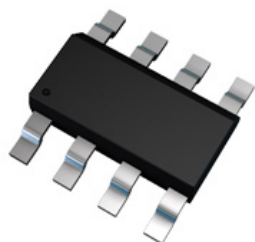
## Description

The ZXMS6004DT8Q is a dual self protected low side MOSFET with logic level input. It integrates over-temperature, over-current, over-voltage (active clamp) and ESD protected logic level functionality. The ZXMS6004DT8Q is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

## Applications

- Two completely isolated independent channels
- Especially suited for loads with a high in-rush current such as lamps and motors
- All types of resistive, inductive and capacitive loads in switching applications
- $\mu C$  compatible power switch for 12V and 24V DC applications.
- Automotive rated
- Replaces electromechanical relays and discrete circuits
- Linear Mode capability - the current-limiting protection circuitry is designed to de-activate at low  $V_{DS}$  to minimise on state power dissipation. The maximum DC operating current is therefore determined by the thermal capability of the package/board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low  $V_{DS}$ .

SM-8



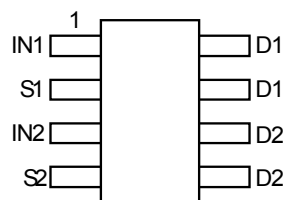
Top View

## Features and Benefits

- Compact Dual Package
- Low Input Current
- Logic Level Input (3.3V and 5V)
- Short Circuit Protection with Auto Restart
- Over Voltage Protection (active clamp)
- Thermal Shutdown with Auto Restart
- Over-Current Protection
- Input Protection (ESD)
- High Continuous Current Rating
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable**
- **Lead-Free Finish; RoHS compliant (Note 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: SM-8
- Case Material: Molded Plastic, "Green" Molding Compound  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.117 grams (approximate)



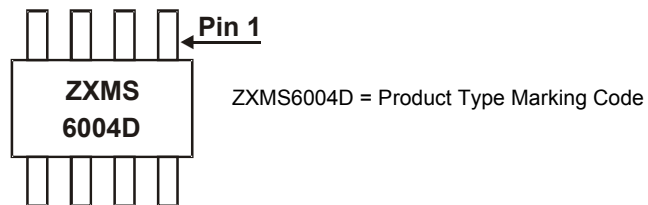
Top view  
Pin-Out

## Ordering Information

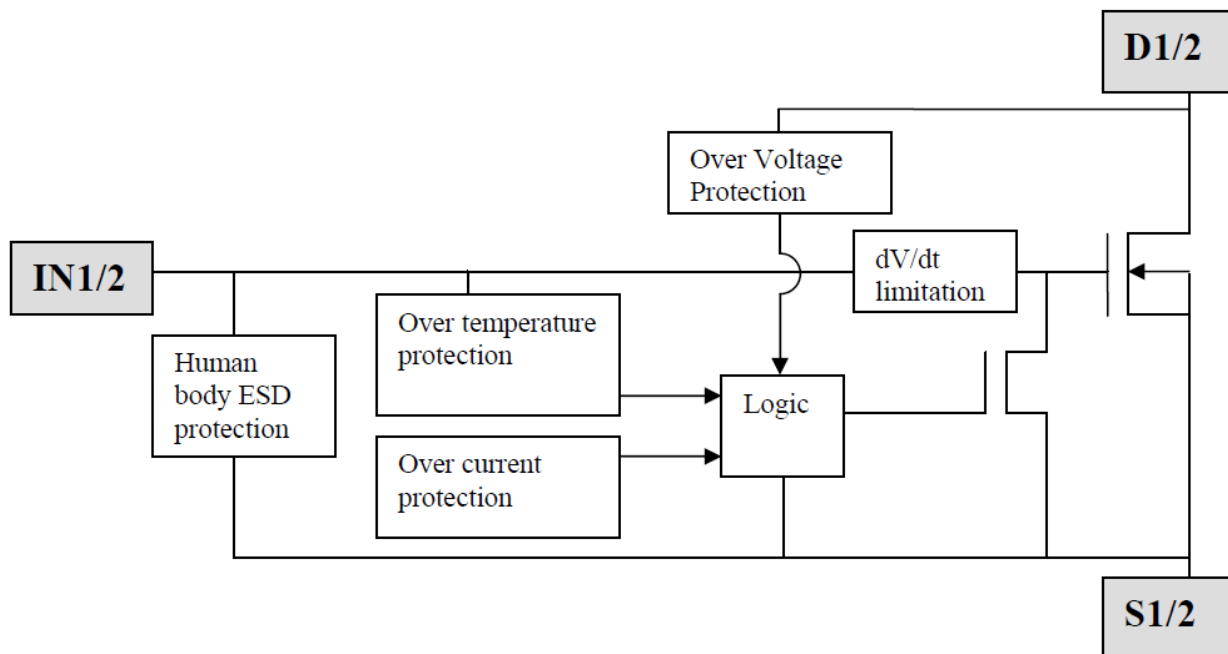
Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMS6004DT8QTA	ZXMS6004D	7	12	1,000 units

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to [http://www.diodes.com/quality/product\\_compliance\\_definitions/](http://www.diodes.com/quality/product_compliance_definitions/).
  5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



## Functional Block Diagram





## Absolute Maximum Ratings (@T<sub>amb</sub> = +25°C, unless otherwise stated.)

Characteristic	Symbol	Value	Units
Continuous Drain-Source Voltage	V <sub>DS</sub>	60	V
Drain-Source Voltage For Short Circuit Protection	V <sub>DS(SC)</sub>	36	V
Continuous Input Voltage	V <sub>IN</sub>	-0.5 ... +6	V
Continuous Input Current @ -0.2V ≤ V <sub>IN</sub> ≤ 6V	I <sub>IN</sub>	No limit	mA
Continuous Input Current @ V <sub>IN</sub> < -0.2V or V <sub>IN</sub> > 6V		I <sub>IN</sub>   ≤ 2	
Pulsed Drain Current @ V <sub>IN</sub> = 3.3V (Note 8)	I <sub>DM</sub>	2	A
Pulsed Drain Current @ V <sub>IN</sub> = 5V (Note 8)	I <sub>DM</sub>	2.5	A
Continuous Source Current (Body Diode) (Note 6)	I <sub>S</sub>	1	A
Pulsed Source Current (Body Diode) (Note 8)	I <sub>SM</sub>	5	A
Unclamped Single Pulse Inductive Energy, T <sub>J</sub> = +25°C, I <sub>D</sub> = 0.5A, V <sub>DD</sub> = 24V	E <sub>AS</sub>	210	mJ
Electrostatic Discharge (Human Body Model)	V <sub>ESD</sub>	4000	V
Charged Device Model	V <sub>CDM</sub>	1000	V

## Thermal Resistance

Characteristic	Symbol	Value	Units
Power Dissipation at T <sub>amb</sub> = +25°C (Notes 6 & 9)	P <sub>D</sub>	1.16	W
Linear Derating Factor		9.28	mW/°C
Power Dissipation at T <sub>amb</sub> = +25°C (Notes 6 & 10)	P <sub>D</sub>	1.67	W
Linear Derating Factor		13.3	mW/°C
Power Dissipation at T <sub>amb</sub> = +25°C (Notes 7 & 9)	P <sub>D</sub>	2.13	W
Linear Derating Factor		17	mW/°C
Thermal Resistance, Junction to Ambient (Notes 6 & 9)	R <sub>θJA</sub>	108	°C/W
Thermal Resistance, Junction to Ambient (Notes 6 & 10)	R <sub>θJA</sub>	75	°C/W
Thermal Resistance, Junction to Case (Notes 7 & 9)	R <sub>θJC</sub>	58.7	°C/W
Thermal Resistance, Junction to Case (Note 11)	R <sub>θJC</sub>	26.5	°C/W
Operating Temperature Range	T <sub>J</sub>	-40 to +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

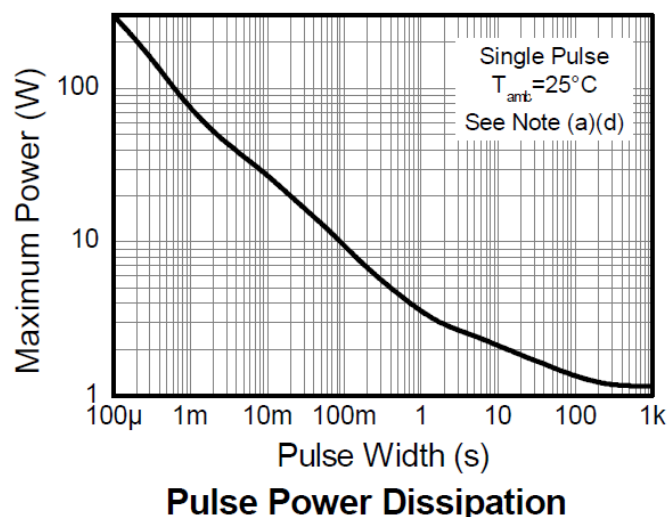
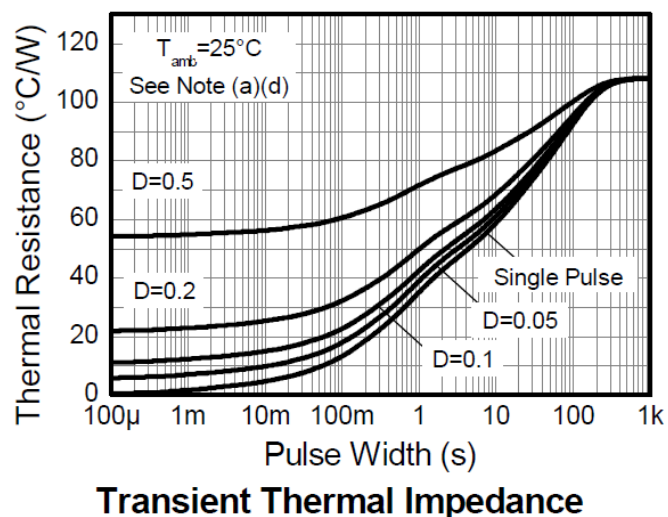
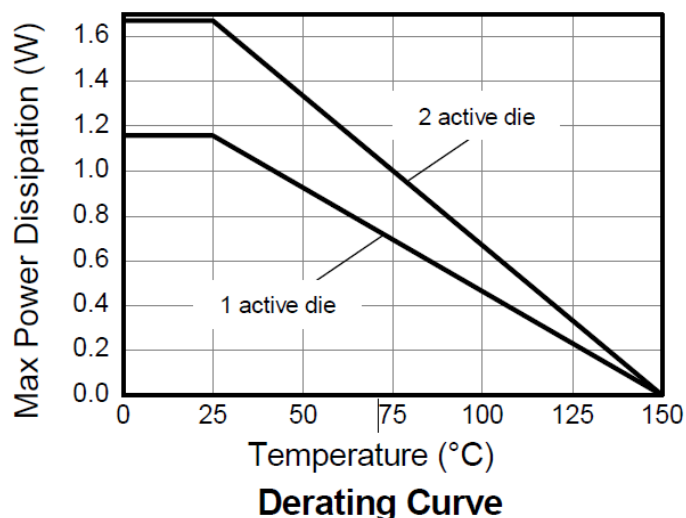
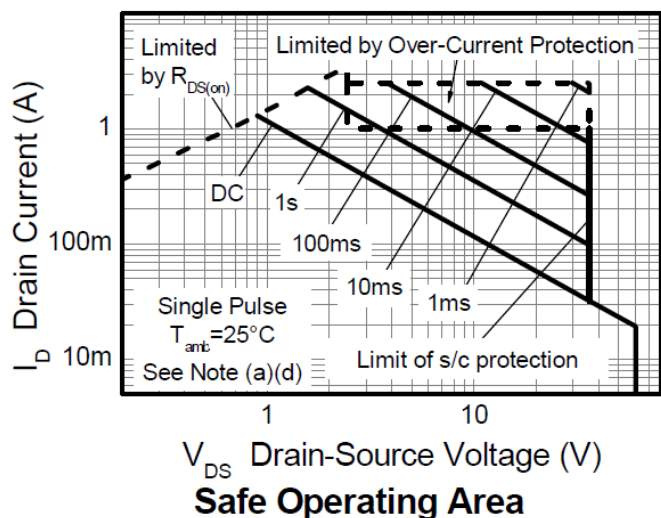
- Notes:
6. For a dual device surface mounted on a 25mm x 25mm FR4 PCB single sided 1oz weight copper split down the middle on 1.6mm FR4 board, in still air conditions
  7. For a dual device surface mounted on FR4 PCB measured at t ≤ 10sec
  8. Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02 pulse width = 300μs – pulse width limited by junction temperature. Refer to transient Thermal Impedance Graph
  9. For a dual device with one active die
  10. For dual device with 2 active die running at equal power
  11. Thermal resistance from junction to solder-point (at the end of the drain lead)

## Recommended Operating Conditions

The ZXMS6004DT8Q is optimized for use with μC operating from 3.3V and 5V supplies.

Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V <sub>IN</sub>	0	5.5	V
Ambient Temperature Range	T <sub>A</sub>	-40	125	°C
High Level Input Voltage for MOSFET to be on	V <sub>IH</sub>	3	5.5	V
Low Level Input Voltage for MOSFET to be off	V <sub>IL</sub>	0	0.7	V
Peripheral Supply Voltage (Voltage to Which Load is Referred)	V <sub>P</sub>	0	36	V

## Thermal Characteristics



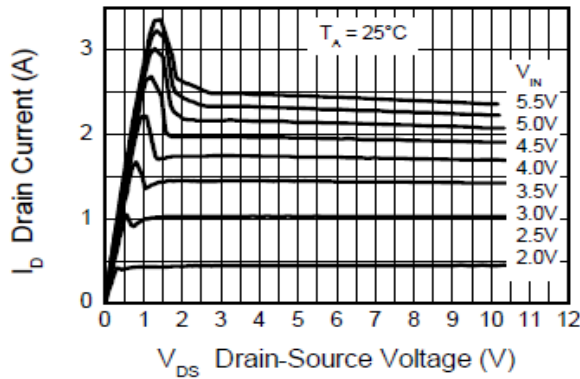
**Electrical Characteristics** (@T<sub>amb</sub> = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Static Characteristics						
Drain-Source Clamp Voltage	V <sub>DS(AZ)</sub>	60	65	70	V	I <sub>D</sub> = 10mA
Off State Drain Current	I <sub>DSS</sub>	—	—	0.5	μA	V <sub>DS</sub> = 12V, V <sub>IN</sub> = 0V
		—	—	1		V <sub>DS</sub> = 36V, V <sub>IN</sub> = 0V
Input Threshold Voltage	V <sub>IN(th)</sub>	0.7	1	1.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1mA
Input Current	I <sub>IN</sub>	—	60	100	μA	V <sub>IN</sub> = +3V
		—	120	200		V <sub>IN</sub> = +5V
Input Current while Over Temperature Active	—	—	—	220	μA	V <sub>IN</sub> = +5V
Static Drain-Source On-State Resistance	R <sub>DS(on)</sub>	—	400	600	mΩ	V <sub>IN</sub> = +3V, I <sub>D</sub> = 1A
		—	350	500		V <sub>IN</sub> = +5V, I <sub>D</sub> = 1A
Continuous Drain Current (Notes 6 & 10)	I <sub>D</sub>	0.9	—	—	A	V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
1		—	—	V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C		
Continuous Drain Current (Notes 6 & 9)		1.1	—	—		V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
		1.2	—	—		V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C
Current Limit (Note 12)	I <sub>D(LIM)</sub>	0.7	1.7	—	A	V <sub>IN</sub> = +3V
		1	2.2	—		V <sub>IN</sub> = +5V
Dynamic Characteristics						
Turn On Delay Time	t <sub>d(on)</sub>	—	5	—	μs	V <sub>DD</sub> = 12V, I <sub>D</sub> = 0.5A, V <sub>GS</sub> = 5V
Rise Time	t <sub>r</sub>	—	10	—	μs	
Turn Off Delay Time	t <sub>d(off)</sub>	—	45	—	μs	
Fall Time	t <sub>f</sub>	—	15	—	μs	
Over-Temperature Protection						
Thermal Overload Trip Temperature (Note 13)	T <sub>JT</sub>	150	175	—	°C	—
Thermal Hysteresis (Note 13)	—	—	10	—	°C	—

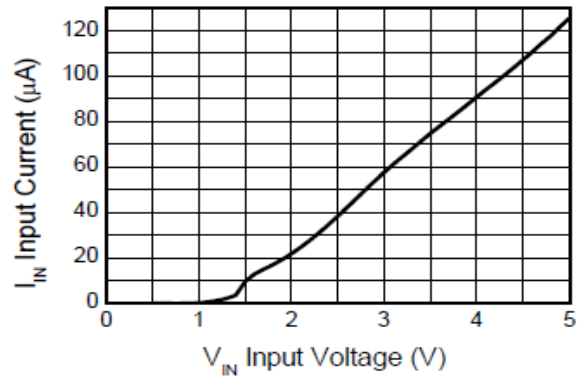
Notes: 12. The drain current is restricted only when the device is in saturation (see graph "Typical Output Characteristic"). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

13. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods.

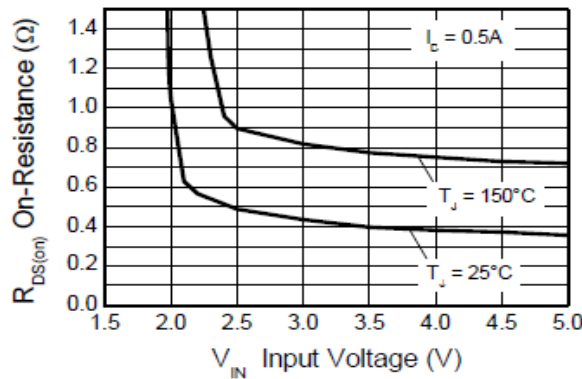
## Typical Characteristics



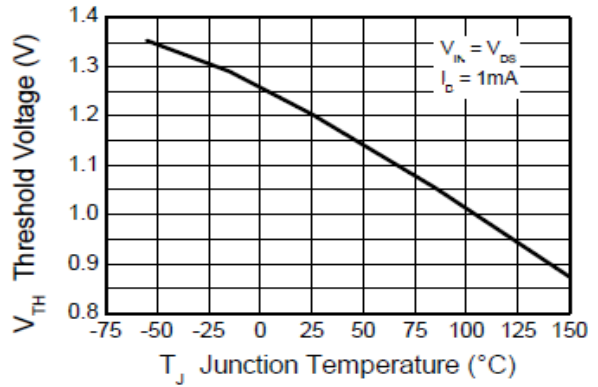
**Typical Output Characteristic**



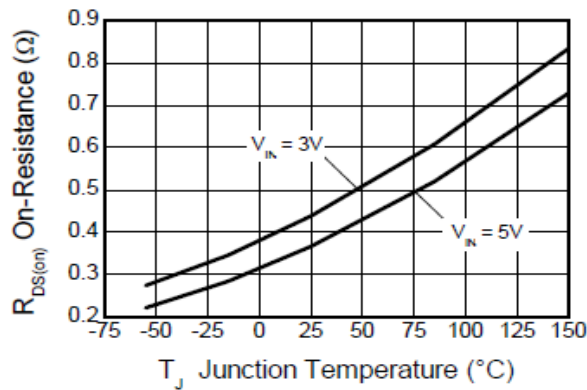
**Input Current vs Input Voltage**



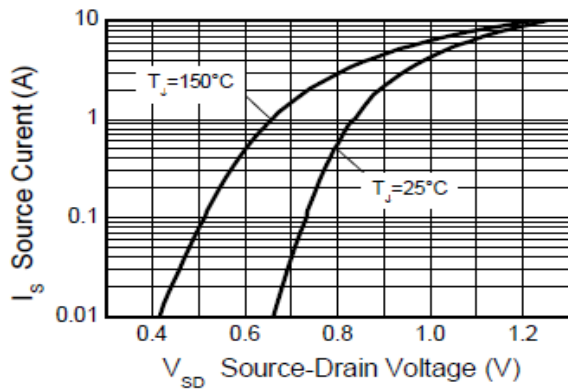
**On-Resistance vs Input Voltage**



**Threshold Voltage vs Temperature**

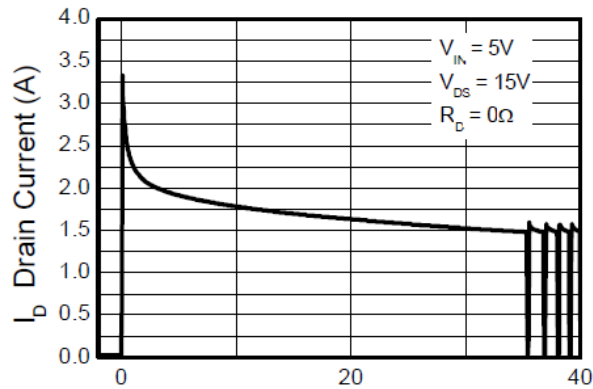
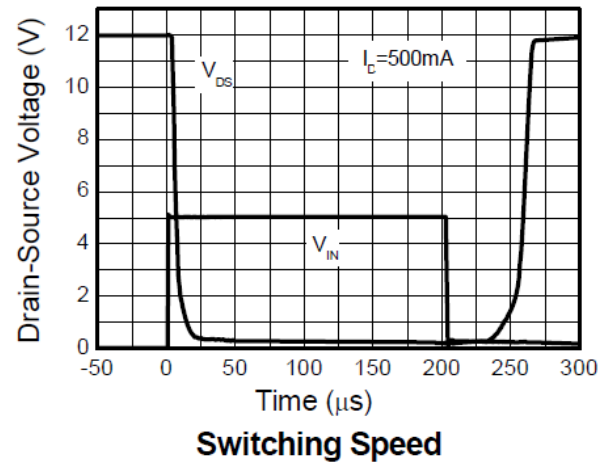
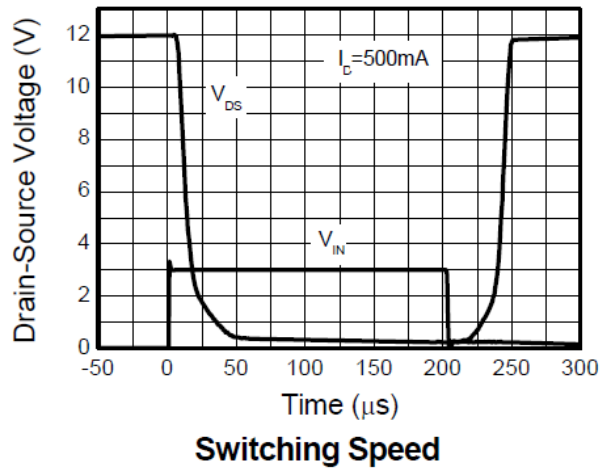


**On-Resistance vs Temperature**



**Reverse Diode Characteristic**

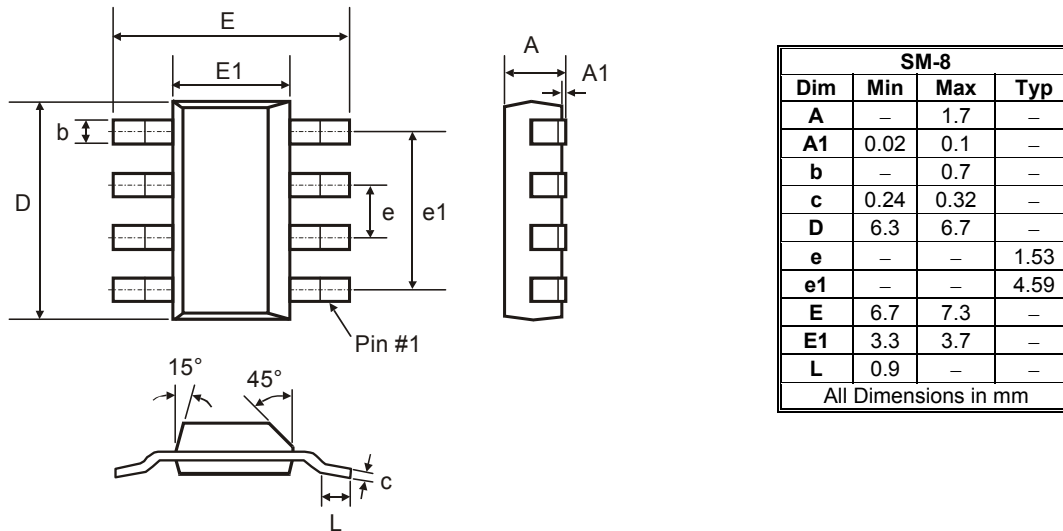
**Typical Characteristics – (cont.)**





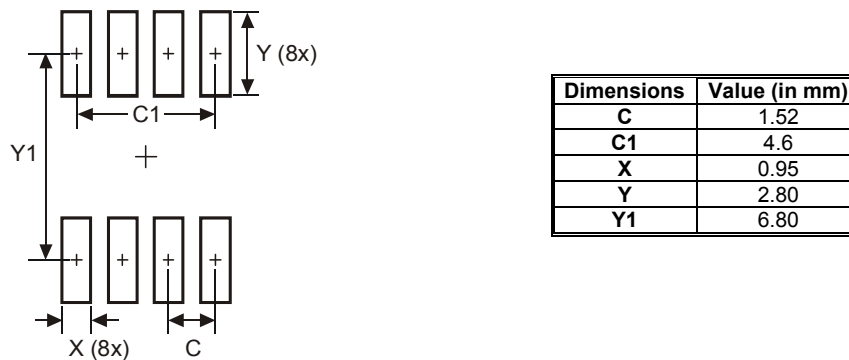
## Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



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