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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH34FS

Non-Inverter Buffer

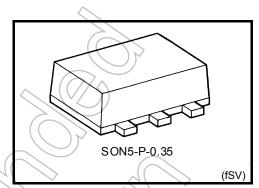
Features

High speed operation : t_{pd} = 3.8 ns (typ.) at V_{CC} = 5V, 15pF
 Low power dissipation : I_{CC} = 2 μA (max) at Ta = 25°C

High noise immunity : V_{NIH} = V_{NIL} = 28% V_{CC} (min)

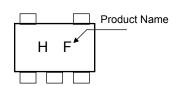
• 5.5-V tolerant input.

• Wide operating voltage range: V_{CC} = 2 to 5.5 V

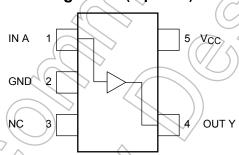


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

	// \ \	31	
Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	– 0.5 to 7	V
DC input voltage	VIN	- 0.5 to 7	V
DC output voltage	V _{OUT}	– 0.5 to V _{CC} + 0.5	V
Input diode current	lik	- 20	mA
Output diode current	lok	± 20 (Note 1) mA
DC output current	TOUT	± 25	mA
DC V _{CC} /ground current	lcc	± 50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	– 65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

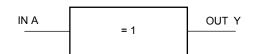
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2004-04

IEC Logic Symbol

Truth Table



Α	Υ
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V _((
Output voltage	V _{OUT}	0 to VCC	V >
Operating temperature	T _{opr}	- 40 to 85	(°C)
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
input rise and rail time	uvuv	0 to 20 ($V_{CC} = 5.0 \pm 0.5 \text{ V}$)	115/

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Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit		
Characteristics	Syllibol	rest condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input			2.0	1.5	_	$\langle \nabla$	1.5	_		
voltage	V _{IH}	_		3.0 to 5.5	V _{CC} × 0.7	_		V _{CC} × 0.7		V
				2.0	_	_	0.5		0.5	V
Low-level input voltage	V_{IL}		_			<u> </u>	VCC × 0.3		V _{CC} × 0.3	
			I _{OH} = -50 μA	2.0	1.9	2.0)	1.9		
High-level output voltage		V _{IN} = V _{IH}		3.0	2.9	3.0) >>	2.9		
	V_{OH}			4.5	4.4	4.5	_	4,4		
			I _{OH} = -4 mA	3.0	2.58	\rightarrow	_	2.48	\forall	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	\rightarrow	- /	3.80	> -	V
Low-level output voltage		$V_{IN} = V_{IL}$	$I_{OL} = 50 \mu A$ $I_{OL} = 4 \text{ mA}$	2.0	$(\vee \neq)$	0.0	0.1) } />	0.1	V
				3.0)	0.0	0.1		0.1	
	V_{OL}			4.5	\searrow	0.0	0.1)	0.1	
				3.0	> _	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_	60	0.36	_	0.44	
Input leakage current	I _{IN}	$V_{IN} = 5.5 \text{ V}$	or GND	0 to 5.5		_\\\	± 0.1	_	± 1.0	μΑ
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$	or GND	5.5			2.0	_	20.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symbol	Test Condi	Ta = 25°C			Ta = -40 to 85°C		Unit		
	Symbol	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time t _{pHL}		3.3 ± 0.3	15	_	5.0	7.1	1.0	8.5	- ns
	t _{pLH}	3.3 ± 0.3	50	_	7.5	10.6	1.0	12.0	
	t _{pHL}	- FOLOF	15	_	3.8	5.5	1.0	6.5	
		5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}	_		_	4	10	ンド	10	pF
Power dissipation capacitance	C _{PD}		(Note 2)		13 ((Z/S)		_	pF

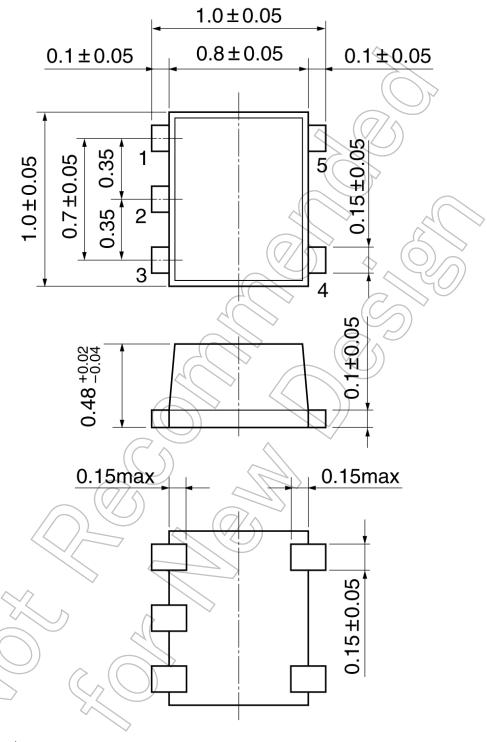
Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.



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Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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